V.R.SIDDHARTHA ENGINEERING COLLEGE::VIJAYAWADA
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
ACTION TAKEN –FEEDBACK THROUGH STAKE HOLDERS

Feedback Collected 2022-23	Action taken in A.Y2023-2024
Student feedback:	
• To reduce network syllabus More models and problems can be added on transients in Network Analysis -2.	• Network syllabus is modified, suggested the course coordinator to cover more problems.
• In Digital Electronics, delete digital logic families concepts.	• Deleted Logic family concepts and the course digital electronics is combined with Analog Electronics.
• Reduce energy meter in Measurements Course	• Removed Energy meter concepts in Measurements (23ES4102B)
• To remove Python course in 2nd sem and to add in 3rd sem, which may help to the laterals also.	• Python course is shifted from 2nd semester to 3rd semester as Skill enhancement course (23EE3651)
• Reduce Some concepts in Synchronous Generator in Electrical Machines-2	• Implemented the suggestion in Unit-I of course title Electrical Machines-II(23EE4305) by removing ZPF method, Parallel operation and two reaction theory of salient pole machine, phasor diagram.
• Add SRM in special machines.	• Implemented the suggestion in Unit-IV and added SRM to special machines of course title Electrical Machines-II(23EE4305)
• Add Java	• Course is offered as an open Elective by CSE/IT departments
• Add some of the courses from the CSE Department.	• Option is given by offering minor degree
• Modify UHV syllabus and Design Thinking	• Suggestion is forwarded to Dean of academics through IQAC cell.
• To provide good experts in training the students in their aptitude and guidance to prepare resume.	• Training in aptitude, reasoning and coding is given to students through external training agencies like Logik works, Six phrase, talent sprint etc and internal faculty of CSE, EEE department and faculty from TNP cell

• Add some of the core related application experiments in micro controller lab • More lab sessions are recommended	• With the help of CSE/EEE department faculty guided in preparing 1minute Video and resume by each student also guided in conducting mock interviews in view of placements. • Suggestion is forwarded to course coordinator and will be taken care while framing third year syllabus of VR23 regulation • It will be taken care by the department.
Faculty Feedback **Linear Integrated and Circuits and Applications** To add IC fabrication process To design square wave and triangular wave with MATLAB/Simulink To design LPF and HPF with MATLAB **Design Thinking** To add difference between Invention and Innovation To add importance of design To add task on collective opinion greater than Expert opinion. To add introduction about TRIZ principles 10,11 and 13. **Power System Analysis** To modify integrated course to theory and introduce lab experiments to simulation lab. **Introduction to smart grid technology** To remove digital signatures and authentications.	**Linear Integrated and Circuits and Applications** 1. To be implement while framing University syllabus 2. Design of square wave and triangular wave are included in curriculum but during class room teaching the wave generators are explained through MATLAB/Simulink 3. Design of LPF and HPF are included in curriculum but during class room teaching the filter designs are explained through MATLAB/Simulink **Design Thinking** Implemented in the curriculum revised for A.Y:2024-25 **Power System Analysis** Suggested to implement the course as integrated course **Introduction to smart grid technology** Implemented the suggestion.
Parent Feedback: 1. To conduct remedial classes to lateral entry students 2. More Industrial visits are required 3. Arrange more number of extra-curricular activities 4. Curriculum design with more software courses 5. Conduct more training programs like Bytexl in next semester 6. Motivate the students for competitive Exams and campus placements.	1. Bridge courses are arranged in mathematics and networks to lateral entry students. Every faculty conducts remedial classes after evaluating their continuous assessment. 2. Industrial visits are arranged every year as per course requirement and encouraged to do internships in Industries during summer. 3. Students are encouraged to join in NCC, NSS and sports. They are also encouraged to participate /act as

	organizing member/volunteer in college function/cultural activities/department events . 4. Minor program in computer science is offered to eligible students. Students are offered with open elective, Programme elective, MOOCs and skill oriented courses relevant to computer science courses. 5. Training programs are arranged for Placements like e-box,CCC, 6. All the students are encouraged to write GATE/GRE/GMAT and competitive examinations through counsellors.
BOS Members: ➤ **Suggestion:** In the process of implementing controllers for Electrical machine it is recommended to carry out few experiments directly on machines without using any digital controllers ➤ To give more emphasis on core courses in depth ➤ To add synchronous generator theory in the course curriculum of Electrical Machines and to be studied before power system courses ➤ To include one Elective course pertaining to digital domain as a basket. ➤ To get hands on experience on advanced topics by conducting workshops/Laboratory.	➤ List of experiments in Electrical Machines lab separated from controllers lab ➤ Advised the faculty whoever taking core courses to implement the suggestion ➤ Implemented the suggestion by adding the content in unit-I of Electrical Machine_II (**23EE4303**) ➤ Suggestion will be taken care while framing Program Elective courses during V and VI semesters ➤ Suggestion is taken care by offering skill enhancement courses / conducting workshops. ➤

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