#### V.R.SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA SIDDHARTHA ACADEMY OF HIGHER EDUCATION Deemed to be University DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

#### PARENTS MEET REPORT

#### A.Y:: 2024-25

Parent's meeting for Second and Third-year students of both sections (A and B) was held on 28<sup>th</sup>September 2024 at the Auditorium, VRSEC from 10.00 AM to 12.30 PM. All the class teachers, UG program coordinator, and student counselors attended the meeting. The objective of the meeting is to discuss about Academic regulations, training programs offered to the students in the campus, placement details and clubs in the department. The parents were informed about the meeting well in advance through student counselors overthe phone.

Dr.P.V.R.L. Narasimham, Professor & Head of the department addressed by welcoming all the parents. He presented the department details, the academic rules and regulations, the continuous evaluation process, credit requirements for promotion and attendance and Industry Institute interaction, internships, placement details. He also explained about training programs offered in campus for students and various technical club activities held in EEE Department. He necessitated the Role of the parent and teacher in achieving good pass percentage among students. HOD suggestedthat all parents monitor the day-to-day activities of their wards. With the initiatives taken by the department and the support of parents only students can achieve academic excellence and get good placements.

Outcome of parents' meeting:

- From II/IV B.Tech 81 parents attended the parent meeting
- From III/IV B.Tech 48 parents attended the parent meeting
- Feedback forms are collected from parents

Parents expressed their happiness and gratitude regarding the care and steps taken by the EEE department in grooming their wards careers and also for the growth of their wards as good citizens in society.

Parents Suggestions:

- · The canteen facility needs to be improved
- Classrooms need some more fans
- Arrange more industrial visits
- Arrange more internships
- Encourage the students to participate in co-curricular activities

A Rama Den:

U.G Programme Co-Ordinator

H.O.D/EEE

Dr.P.V.R.L.NARASIMHAM Professor & Head Dept.er Electrical & Electronics Engineering V.R.Siddnartha Engineering College VIJAYAWADA-520 007.

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Following faculty members attendedII/IV &III/IV B.Techstudent'sparents meeting conducted on 28th September,2024 at Auditorium, VRSEC from 10.00AM to 12.30PM.

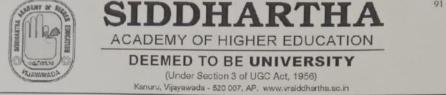
S.No	Name of the Employee	Designation	Signature
1.	Dr.P.V.R.L.Narasimham	Professor& Head	pronoring
2.	Dr.A.Ramadevi	Professor	ARO
3.	Dr.B.VenkateswaraRao	Assoc. Professor	Trusage
4.	Dr D .Indira	Asst.Professor	RI
5.	Dr.T. Naveen	Asst.Professor	VX
6.	Dr.N.Vamsi Krishna	Sr.Asst.Professor	2
7.	Dr.Subhojit Dawn	Sr.Asst.Professor	90
8.	Dr J VimalaKumari	Asst.Professor	Oy
9.	Mrs V Bindu	Asst.Professor	1
10	MrM.L.N.Vital	Asst.Professor	
11	Dr.J Ramesh	Assoc. Professor	
12	MrsS.V.R.LakshmiKumari	Assoc. Professor	le
13	T.Sunil	Asst.Professor	TEL
14	Mrs.K.Lalitha	Asst.Professor	1
	MrS.N.V.S.K.Chaitanya	Asst.Professor	apo,
	MrR.MadhusudhanaRao	Asst.Professor	Rhadty
17	V.Harivamsi	Asst.Professor	
	V.Ravindranath	Asst.Professor	a

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U.G Programme Co-Ordinator

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Dr. P. V. R. L. NARASIMHAM Professor & Head Dept.of Electrical & Electronics Engineering V.R. Siddhartha Engineering College VIJAYAWADA-520.007 VELAGAPUDI RAMAKRISHNA SCHOOL OF ENGINEERING



91 866 2582333 866 2582334 866 2584930

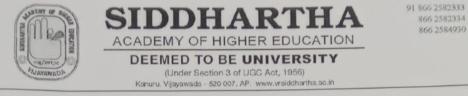
**Department of Electrical & Electronics Engineering** 

Date: 23/09/2024

### Circular

It is to inform to all the faculty members that the II/IV and III/IV B.Tech students parents meeting will be held on 28/9/2024 (Saturday) in auditorium hall at 10AM. In this context second and third year of both section A and B student counsellors and class teachers must attend the meeting. The counsellors should inform well in advance to respective parents about this meeting.

Dr. P.V.R.L Narasimham Professor, HOD, EEE Dr.P.V.R.L.NARASIMHAM Professor & Head Dept.of Electrical & Electronics Engineering V.R.Siddhartha Engineering Colleg VIJAYAWADA-520 007. VELAGAPUDI RAMAKRISHNA SCHOOL OF ENGINEERING



## Department of Electrical & Electronics Engineering

Date: 23/09/2024

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#### Circular

It is to inform to all the parents of II/IV and III/IV B.Tech students that parents meeting will be held on 28/9/2024 (Saturday) in auditorium hall at 10AM . In this context second and third year parents are requested to attend the meeting without fail. The purpose of the meeting is to explain about

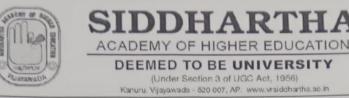
- 1. Academic regulations
- 2. Training programs offered to students in campus
- 3. Placement details
- 4. Clubs in the department

2024 Appener on 23 Dr. P.V.R.L Narasimham

Professor, HOD, EEE

Dr.P.V.R.L.NARASIMHAM Professor & Head Dept of Electrical & Electronics Engineering V.R.Siddhartha Engineering Colleg-VIJAYAWADA-520 UNT

#### VELAGAPUDI RAMAKRISHNA SCHOOL OF ENGINEERING



91 866 2582333 866 2582334 866 2584930

# Department of Electrical & Electronics Engineering

Date: 23/09/2024

## Circular

II/IV మరియు III/IV B.Tech విద్యార్థుల తెల్లిదండ్రులందరికీ ఈ సెల 28/9/2024 (శనివారం) ఆడిటోరియం హాల్లో ఉదయం 10 గంటలకు తెల్లిదండ్రుల సమాపేశం నిర్వహించబడుతుందని తెలియజేస్తున్నాము. ఈ సేపథ్యంలో సెక్షన్ A మరియు B రెండింటిలో రెండవ మరియు మూడవ సంవత్సరాల తెల్లిదండ్రులు తప్పనిసరిగా సమాపేశానికి హాజరు కావాలి. ఈ సమాపేశం యొక్క ముఖ్య ఉద్దేశం కింది అంశాలను వివరించడానికి

- 1. విద్యాసంబంధ నిబంధనలు
- 2. క్యాంపస్ లో విద్యార్థులకు అందించే శికణా కార్యక్రమాలు
- 3. ప్లేస్మెంట్ వివరాలు
- 4. విభాగంలో క్లబ్బులు

07/9446257

Dr. P.V.R.L Narasimham

Professor, HOD, EEE

Dr P.V.R L. NARASIMHAM

Professor & Head Dept of Electrical & Electronics Engineering V.R. Siddhartha Engineering College VIJAYAWADA-520.007

#### **28-10-2024 PARENTS MEET PHOTOS**





28/09/24 11:27 AM GMT +05:30

Google



