

Dr. M Padmaja



Designation : Professor and Programme Coordinator(ECE)
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Contact at : EC304, Department of Electronics and Communication Engineering,
 Velagapudi Ramakrishna Siddhartha Engineering College,
 Kanuru, Vijayawada, Andhra Pradesh 520 007.

Education

Ph. D (Signal Processing and Communications)

2013, Sri Venkateswara University, Tirupathi, AP.

M.S (Electronics and Controls)

1996, BITS, Pilani

B.Tech (Electronics and Communication Engineering)

1988, Nagarjuna University, Nagarjuna Nagar, Guntur dist., AP

Experience

Period	Designation	Institution / Organization
2014 to Till date	Professor	Velagapudi Ramakrishna Siddhartha Engineering College
2005-2014	Associate Professor	Velagapudi Ramakrishna Siddhartha Engineering College
2004-2005	Sr. Lecturer	Velagapudi Ramakrishna Siddhartha Engineering College
1996-2003	Lecturer	Velagapudi Ramakrishna Siddhartha Engineering College
1993-1996	Associate Lecturer	KES Polytechnic for Women, Vijayawada
1988-1992	Software Engineer	Siva Systems, Vijayawada

Research Interests

Broad Area of Research :

- Signal Processing, Image Processing, Wireless Communications, Remote sensing Applications

Honors and Awards

- Best Performance Award received from Vice Chancellor, JNTU Kakinada for 2012-2013 on the occasion of JNTUK foundation day, 21.8.2012.
- AICTE sponsored Rs 2,00,000/- for “Staff Development Program” on “Signal Processing and Wireless Communications” during 13th to 25th July, 2009

Courses Taught

UG:

Electronic Devices and Circuits
Electronic Circuit Design and Analysis
Signals and Systems
Probability and Random Variables
Microprocessors and Interfacing
Digital Communications
Digital Signal Processing
Electronic Measurements and Instrumentation
Mobile and Cellular Communications

DSP Processors and Architectures
Advanced Communications

PG:

Advanced Digital Communications
Probability and Random Processes
Coding Theory and Techniques
Microwave and Satellite Communications
Adhoc Networks
Wireless Sensor Networks
ARM Controllers for Embedded Systems

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Scopus	https://www.scopus.com/authid/detail.uri?authorId=23985728500
ORCID	https://orcid.org/0000-0002-5360-0937
Research Gate	https://www.researchgate.net/profile/Padmaja_Mididoddi
LinkedIn	https://www.linkedin.com/in/padmaja-m-985b1a12

Recognized Research Supervisor

S.No.	Department	University
01.	Electronics and Communication Engineering	Jawaharlal Nehru Technological University, Kakinada

Funded Research Projects

Completed	02
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Patents

Applied	01
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Invited Talks

No. of Invited Talks	02
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Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	10
International / National Level Seminars	15 / 04
International / National Level Conferences	12 / 06
Workshops	30
Instructor Led / Self-Paced Courses	07
Webinars	08

Professional Bodies Membership Details

FIETE	Institution of Electronics and Telecommunication Engineers
MISTE	Indian Society for Technical Education (ISTE)
MIE (I),	Institution of Engineers(India)
MBMESI	The Biomedical Engineering Society of India
MSEMCEI	The Society of EMC Engineers (India),

(Dr. M Padmaja)
