

R V H Prasad



Designation: Assistant Professor

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Contact at : EC 352, Department of Electronics and communication Engineering,

Velagapudi Ramakrishna Siddhartha Engineering College,

Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

Ph. D (Electronics and Communication Engineering)

National Institute of Technology, Andhrapradesh (NIT-AP)

M.Tech (Electronics and Communication Engineering)

2008, Anna University, Chennai

B.Tech (Electronics and Communication Engineering)

2005, Jawaharlal Nehru Technological University, Hyderabad

Experience

Period	Designation	Institution / Organization
2011 to Till date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College
2008-2011	Asst. System Engineer	TATA Consultancy Services (TCS)

Research Interests

Broad Area of Research:

VLSI Signal Processing, Compressive Sensing

Courses Taught

- Digital Circuits and Systems
- Computer Organization and Architecture
- Microprocessor and Microcontrlers
- Principles of Embedded Systems

- Real Time Operating System
- Advanced Computer Architecture
- CPLD and FPGA Architectures
- Real Time Systems

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Scopus	https://www.scopus.com/freelookup/form/author.uri?st1=Prasad%20Reddy&st2 =V%20H	
ORCID	https://orcid.org/0000-0003-0417-3956	
Publons	https://publons.com/researcher/3701778/rvh-prasad/	
Research Gate	https://www.researchgate.net/profile/V_H_Prasad_Reddy	
LinkedIn	https://www.linkedin.com/in/v-h-prasad-reddy-437b2763/	

Funded Research Projects

Completed 02

Professional Development Activities - Participations

Faculty Development and Training Programmes	10
(at least 5 Days)	
International / National Level Seminars	01/ 05
Workshops	14
Instructor Led / Self-Paced Courses	09
Webinars	07

(R V H Prasad)
