

V B K L Aruna



Designation: Assistant Professor

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Contact at: EC 356, Department of Electronics and Communication Engineering,

Velagapudi Ramakrishna Siddhartha Engineering College,

Kanuru, Vijayawada, Andhra Pradesh 520007.

Education

Ph. D, Pursuing (Electronics and Communication Engineering)

SRM Institute of Science and Technology

M.Tech (Digital Electronics and Communication Engineering)

2011, Jawaharlal Nehru Technological University, Kakinada

B.Tech (Electronics and Communication Engineering)

2008, Jawaharlal Nehru Technological University, Hyderabad

Experience

Period	Designation	Institution / Organization
2012 to Till date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College
2010-2012	Assistant Professor	Vijaya Institute of Technology, Vijayawada
2008-2010	Assistant Professor	GMR Institute of Technology, Rajam
2008	Content Developer	SIS Pvt Ltd. Visakhapatnam

Research Interests

Broad Area of Research:

• Low Power VLSI Domain

Courses Taught

- Computer Networks
- VLSI Technology
- Low Power VLSI Design
- Telecoomunications

- Analog Electronics
- Adhoc Networks
- Data Communications
- Digital Electronics
- Switching Theory and Logic Design
- Digital Integrated Circuits and Aplications

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Scopus	https://www.scopus.com/authid/detail.uri?authorId=57201944157
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Professional Development Activities - Participations

Faculty Development and Training Programmes (at least 5 Days)	03
International / National Level Seminars	03
International / National Level Conferences	05
Workshops	17
Webinars	03

Professional Bodies Membership Details

3853644	IEEE Member
	ISTE Member

(VBKLAruna)
