

Dr. Gummadi Lakshmi Madhumati



Designation: Professor

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Velagapudi Ramakrishna Siddhartha Engineering College, Kanuru,

Vijayawada, Andhra Pradesh, 520007.

Education

Ph.D(Electronics and Communication Engineering)

2011, Jawaharlal Nehru Technological University Hyderabad, Kukatpally, Hyderabad

M.Tech(Digital Systems and Computer Electronics)

2003, Jawaharlal Nehru Technological University, Kukatpally, Hyderabad.

B.E(Electronics)

1991, Shivaji University, Kolhapur, Maharashtra

Experience

Period	Designation	Institution/Organization
February 2022	Professor,	Velagapudi Ramakrishna Siddhartha Engineering College,
to Till date	Dept. of ECE	Vijayawada
2011-2022	Professor & Head, Dept. of ECE	Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada
2011-2011	Professor, Dept. of ECE	Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
2010-2011	Professor, Dept. of ECM	Prasad V Potluri Siddhartha Institute of Technology, Vijayawada
2007-2010	Associate Professor, Dept. of ECM	Prasad V Potluri Siddhartha Institute of Technology, Vijayawada
2005-2007	Lecturer, Dept. of ECE	Koneru Lakshmaiah College of Engineering, Greenfields, Vaddeswaram, Vijayawada
2002-2005	Lecturer, Dept. of ECE	Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
1993 - 2001	Associative Lecturer, Dept. of ECE	V.K.R and V.N.B Polytechnic, Gudivada

Research Interests

Broad Area of Research:

VLSI Design, Digital design using HDLs	Mixed signal VLSI design
Physical design,Low Power VLSI Design	Wireless Communication
DSP Architectures ,Image processing	Partial Reconfiguration, System on Chip
Architectures and Bio Medical Applications	

Courses Taught

Undergraduate Level	Postgraduate Level	
Switching Theory & Logic Design	Digital System Design	
Digital System Design & Digital IC Applications	VLSI Technology and Design	
VLSI Design	Digital Design using HDL	
Digital Image Processing	CMOS Analog IC Design	
Computer Architecture and Organization	CMOS Digital IC design	
Operating Systems	CPLD and FPGA Architectures and Applications	
Electronic Devices and Circuits	Low Power VLSI Design	
Electronic Circuit Analysis	CMOS Mixed Signal Circuit Design	
Pulse & Digital Circuits	System on Chip Design	
Analog Communication	Nano-electronics	
Satellite Communication	Hardware Software co-design	
System on Chip		
System Design Through Verilog		

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?hl=en&user=uUQDcu0AAAAI	
Scopus	https://www.scopus.com/authid/detail.uri?authorId=35174862800	
Orcid	https://orcid.org/0000-0002-1547-2583	
LinkedIn	https://www.linkedin.com/in/gummadi-lakshmi-madhumati-7ba7a22b/	

Recognized Research Supervisor

S.No.	Department	University
	Electronics and Communication Engineering	KL University, Greenfields, Vaddeswaram, Vijayawada
	Electronics and Communication Engineering	Jawaharlal Nehru Technological University,Kakinada

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Invited Talks

No. of Invited Talks	05

Professional Service/Reviewer

- Research Advisory Committee (RAC) member at Sardar Patel Institute of Technology, Andheri (W), Mumbai-58.
- Reviewer for IEEE conference on "IoT for Rural Healthcare 2021" (CIRH-2021) organized by VFSTR (Deemed to be University), Guntur, India.
- BOS Member of Department of Electronics at P.B. Siddhartha College of Arts & Science (Autonomous), Vijayawada-10.

Professional Development Activities-Participations

Faculty Development and Training Programmes (atleast 5Days)	8
International/National Level Conferences	10
Workshops	10
Instructor Led/Self-Paced Courses	1

Professional Bodies Membership Details

LMISTE	Life Member of The Indian Society for Technical Education
LMISSE	Indian Society of Systems for Science & Engineering (ISSE) Life Membership
IFERP	Member of Institute for Engineering Research and Publication
MIEEE	Member of IEEE Advancing Technology or Humanity

(Dr.G.L.Madhumati)
