

Velagapudi Ramakrishna Siddhartha Engineering Department of Electronics & Communication Engineering News Letter

Volume 10 Issue 2

Sep Oct 2022



Events organized in the department

A Guest Lecture was conducted by Dr. A. Jhansi Rani, Professor, Ch. Raghavendra, Assistant Professor ECE, on "Design and Implementation of Antenna Array synthesis & pattern tenability for Bandwidth Enhancement" on 20th September 2022 for which Dr. S. Yuvaraj, Assistant Professor, Department of Electronics and Communication Engineering, National Institute of Technology Andhra Pradesh was the resource person. Dr.S. Yuvaraj delivered sessions on Overview of antenna and its parameters, antenna array synthesis & pattern tenability for Bandwidth Enhancement design concepts. He also explained about Antenna parameters, Arrays, Uniform array, Planar Array, Phased Array, Antenna Beam Forming, Next Generation Communication Challenges, Reconfigurable Meta surfaces, Reflect antenna Array. This Guest Lecture provided an opportunity for faculty and students to have an exposure for doing Research in this antenna domain. Faculty had an opportunity to interact with resource person and other participants which can help them in their professional career.UG Students were exposed to Design and Implementation of Antenna Array synthesis & pattern tenability for Bandwidth Enhancement









FDPs/Workshops/Seminars attended by the faculty

- Dr. A. Jhansi Rani attended Guest Lecture on Design and implementation of Array synthesis & pattern tunability for Bandwidth Enhancement organized by ECE Department, Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada on 20.09.2022.
- Mr. Ch. Raghavendra attended Guest Lecture on Design and implementation of Array synthesis & pattern tunability for Bandwidth Enhancement organized by ECE Department, Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada on 20.09.2022.
- Mrs.V. Saritha attended Expert lecture on "Microstrip Antenna Design and Optimization using Full Wave Simulator organized by IEEE APS SBC MNIT in association with IEEE SB MNIT, IEEE RFIDSBC MNIT & IEEE Delhi APS Chapter-Jaipur on 03.09. 2022.
- Mrs.V. Saritha attended Guest Lecture on Design and implementation of Array synthesis & pattern tunability for Bandwidth Enhancement organized by ECE Department, Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada on 20.09.2022.
- B. Alekhya, attended Guest Lecture on Design and implementation of Array synthesis & pattern tunability for Bandwidth Enhancement organized by ECE Department, Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada on 20.09.2022.
- Dr. K. Shri Ramtej attended All India Seminar on "5G -The New Radio organized by PVP Siddhartha Institute of Technology during 14.10.2022 to 15.10.2022.
- Mrs.S.N. Malleswari attended Guest Lecture on Design and implementation of Array synthesis & pattern tunability for Bandwidth Enhancement organized by ECE Department, Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada on 20.09.2022.
- B. Alekhya, attended All India Seminar on "5G -The New Radio organized by PVP Siddhartha Institute of Technology during 14.10.2022 to 15.10.2022.
- B. Alekhya attended Expert lecture on "Microstrip Antenna Design and Optimization using Full Wave Simulator organized by IEEE APS SBC MNIT in association with IEEE SB MNIT, IEEE RFIDSBC MNIT & IEEE Delhi APS Chapter-Jaipur on 03.09. 2022
- Dr. K. Shri Ramtej attended 3-day FDP on "Machine Learning and Deep Learning for Communication Systems using MATLAB and Simulink" organized by Hyderabad Institute of Technology and Management in association with MathWorks, Wiley and Taylor & Francis held from 20-22 Oct 2022.

Achievement by faculty

Dr.G.L.Madhumati, Professor, ECE was nominated as Research Coordinator in the department of Electronics and Communication Engineering of SRM Institute of Science and Technology (Formerly Known as SRM University) SRM Nagar, Kattankulathur, Tamilnadu.



Students awards under the guidance of faculty

On the occasion of India's 75th year of Independence, NXP Semiconductors India launched a unique program to welcome the Women Change makers – WOMEN IN TECH (WIT) program. It is a very unique Scholarship & Mentorship Program for top talented women engineers at a very early stage in their engineering studies! After a rigorous evaluation of 5000+ registrations across India, the Top 50 women students have been selected for the first-ever batch of the WIT program by NXP Semiconductors. The year-long program intends to give students the exposure and opportunities to make a difference in the semiconductor industry. This will be a hybrid program encompassing theory, industry-relevant use cases and practical classes on SoC Architecture, Analog Design, Design Architecture, Verilog/System Verilog, Embedded Software, Verification & Validation, RISC-V and DFT Basics.

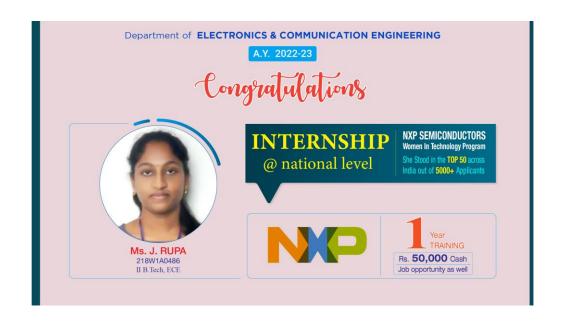


Ms J Rupa 218W1A0486 II BTech ECE

Internship provides Yearlong semiconductor focused technical classes by industry experts for enabling innovation Focus on industry-relevant application oriented practical sessions with concepts Internship & Job opportunities at NXP Opportunity to build a network with silicon tech mentors Monetary reward worth INR. 50,000.

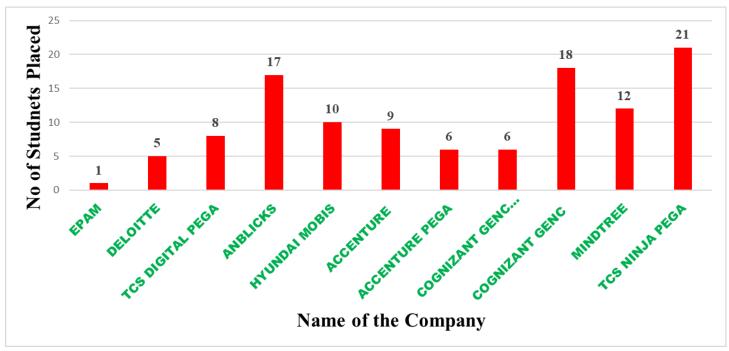
Ms J Rupa (218W1A0486) of II BTech ECE got selected for NXP Semiconductors Women in Technology program! She stood in the top 50 across India out of 5000+ applicants!

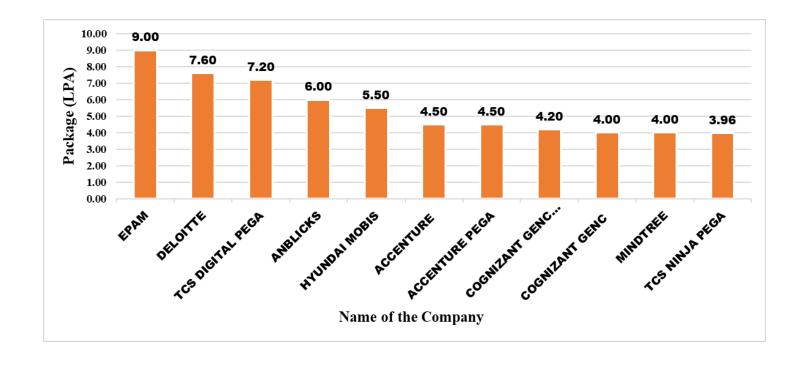
She will receive 1 yearlong training, internship, Rs 50,000 cash, job opportunity as well.



Placement Details..... Sep & Oct 2022







Chief Editor

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