

# Dr. V. Narasimha Nayak



**Designation** : Assistant Professor

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**Contact at** : EC339, Department of Electronics and Communication Engineering,  
Velagapudi Ramakrishna Siddhartha Engineering College,  
Kanuru, Vijayawada, Andhra Pradesh 520007.

## Education

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### **Ph. D (Electronics and Communication Engineering)**

2023, NIT Andhra Pradesh, Tadepaliigudem

### **M.Tech (Electronics and Communication Engineering)**

2010, NIT Rourkela, Rourkela

### **B.Tech (Electronics and Communication Engineering)**

2007, Jawaharlal Nehru Technological University, Hyderabad

## Experience

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Period	Designation	Institution / Organization
2022-till now	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College
2019-2022	Teaching Assistant	NIT Andhra Pradesh
2011-2019	Assistant Professor	KL University, Vijayawada
2010-2011	Assistant Professor	Vijaya Engineering College

## Research Interests

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### **Broad Area of Research :**

NOMA, VLSI Design, Physical Layer Security, Cooperative Communications

## Honors and Awards

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- Received HTRA under Institute Scholar (NIT Andhra Pradesh), Govt. of India, 2019-2022.
- Received Institute Fellowship in M.Tech, through GATE , MHRD, Govt. of India, 2010-2012.
- Ratified Assistant Professor by KL University, Andhra Pradesh.

## Courses Taught

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- CMOS VLSI Design
- Digital Logic Design
- Electronic Devices and Circuits
- Design with PLD's and FPGA's
- Analog Electronic Circuits
- Linear IC's and Analysis
- Analog VLSI Design
- Pulse and Digital Circuits
- VLSI Sub System Design

## Research Profile

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### Publications

The recent publications and research contributions can be viewed from the following URLs

<b>Google Scholar</b>	<a href="https://scholar.google.com/citations?user=W9d_LBgAAAAJ&amp;hl=en&amp;oi=sra">https://scholar.google.com/citations?user=W9d_LBgAAAAJ&amp;hl=en&amp;oi=sra</a>
<b>Scopus</b>	<a href="https://www.scopus.com/authid/detail.uri?authorId=57202640258">https://www.scopus.com/authid/detail.uri?authorId=57202640258</a>
<b>ORCID</b>	<a href="https://orcid.org/0000-0001-8230-8030">https://orcid.org/0000-0001-8230-8030</a>
<b>Publons</b>	<a href="https://publons.com/wos-op/researcher/5378315/narasimha-nayak-v">https://publons.com/wos-op/researcher/5378315/narasimha-nayak-v</a>
<b>Research Gate</b>	<a href="https://www.researchgate.net/scientific-contributions/V-Narasimha-Nayak-2174525754">https://www.researchgate.net/scientific-contributions/V-Narasimha-Nayak-2174525754</a>
<b>LinkedIn</b>	<a href="https://www.linkedin.com/in/narasimha-nayak-8b02611b5/">https://www.linkedin.com/in/narasimha-nayak-8b02611b5/</a>

### Professional Service / Reviewer

- Reviewer for IEEE Transactions on vehicular technology
- Reviewer for Transactions on Emerging Telecommunication Technologies (Wiley)
- Reviewer for International Journal of Electronics

## Professional Development Activities - Participations

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Faculty Development and Training Programmes (at least 5 Days)	08
International / National Level Seminars	00
International / National Level Conferences	03/ 01
Workshops	08
Instructor Led / Self-Paced Courses	01
Webinars	12

## Professional Bodies Membership Details

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LM81317	Indian Society for Technical Education (ISTE)
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(Dr. V. Narasimha Nayak)

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