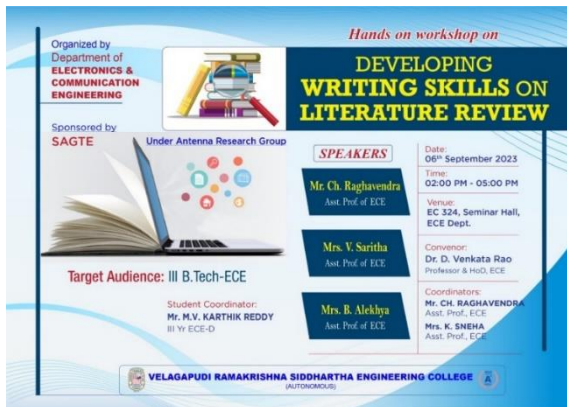




National Seminar

“Developing Writing Skills on Literature Review”

National Seminar on “Developing Writing Skills on Literature Review” was organized by the Dept of ECE on 6th Sep 2023. The speakers gave explanation about the types of publications, review articles, types of review articles, literature, literature reviews, rules to write literature reviews, research papers, construction of research papers, paper submission guidelines, literature, literature review, how to avoid plagiarism, paper review process. This Workshop provided an opportunity for the students to have an exposure in Research Writing and Paper Publication. It acted as a forum to further widen their knowledge and to pursue research studies. Students were motivated to do the open-ended and societal problems related to their research domain which can be used in Next Generation Technological Challenges. Students had an opportunity to interact with the Speakers which could help them in their professional career. UG Students were exposed to Research Writing and Literature Review Processes which can be helpful to them in their Academic and Professional lives.



One Day Guest Lecture

“Implementation of AI and ML Algorithms using Weka Tool”

This One Day Guest Lecture on “Implementation of AI and ML Algorithms using Weka Tool” provided an opportunity for III & IV B. Tech students to have an exposure to the Weka tool, its features, and capabilities for implementing AI and ML algorithms. Weka is a widely used and popular open-source machine learning software tool that provides a collection of algorithms and tools for data mining and machine learning tasks. Developed at the University of Waikato in New Zealand, Weka (which stands for Waikato Environment for Knowledge Analysis) offers a user-friendly graphical interface and a comprehensive set of machine learning algorithms, making it accessible to both beginners and experienced data scientists. Students were introduced to the necessity of Wavelet transforms and their application in extracting important features. Using the extracted features to build ML models by using Weka tool.



FDPs/Workshops/Guest lecture attended by faculty

- Dr. Jasti Sateesh attended 'INUP- i2i Hands-on Training Workshop on Fabrication and Characterization of 2D MoS₂ Transistor' during 09-10-2023 to 18-10-2023 at IIT Bombay.
- Dr. K. Shri Ramtej, has participated in a One-week Online Faculty Development Programme (FDP) on “AI Enabled AR/VR in Communications & Signal Processing Application”, during 3rd to 7th October 2023, organized by Department of Electronics and Communication Engineering, Nalla Narasimha Reddy Education Society’s Group of Institutions – Integrated Campus.
- K. Sneha has participated in an Eight-day National Faculty Development Program on “Case based learning, teaching, writing and publication” held from 25-9-23 to 2-10-23, organized by Department of Management Studies, Vignan’s (VFSTR) deemed to be university, Vadlamudi, Andhra Pradesh.

Publications

- K Shri Ramtej, Venkata Sainath Gupta T, G Suryanarayana, Karthik Chandran, V Siva Reddy, Sahithi Bhuvana V., Anitha Y., Yogitha V. and Valli Reshma P., Chest CT Image Based Lung Disease Classification – A Review, Current Medical Imaging 2024; 20: e161023222266 . <https://dx.doi.org/10.2174/0115734056248176230923143105>
- M. A. Halimi, T. Khan, A. A. Kishk, Y. M. M. Antar, “Efficient rectifier circuit operating at N78 and N79 sub-6 GHz 5G bands for microwave energy-harvesting and power transfer applications” International Journal of Microwave and Wireless Technologies; pp. 1-8, 2023. <https://doi.org/10.1017/S1759078723001162>.

Patents filed/awarded

- Patent filed by Ch. Raghavendra, “Deep learning techniques for prevention of train accident and prediction of breakdown in the system”. The Patent Office Journal No. 39/2023 Dated 29/09/2023. Application No.202321047280 A. Date of filing of Application :13/07/2023. Publication Date: 29/09/2023

MoU

- A Memorandum of Understanding (MoU) was established between Velagapudi Ramakrishna Siddhartha Engineering College and INUP IIT Bombay. The MoU allows the students and faculty to utilize infrastructure available at Nanofabrication Lab at IIT Bombay. It also provides faculty to undertake collaborative rese, introduce new course which will be handled by IIT Bombay faculty, and internships for UG and PG students. The MoU will be in force till the INUP 3rd phase is completed. The contact persons in IIT Bombay and VRSEC are Dr. K. Nageswari and Dr. Jasti Sateesh respectively.

Students Corner

- Ms. A. Vyshnavi II/IV B. Tech ECE (228W1A0404) got selected for internship in NXP Women in Technology Programme (Batch-II). She is one among the 60 selected students out of 8500+ students from all over India including all premier institutes like IITs and Universities competed!
- Ms. K.Saranya of III/IV B.Tech ECE presented a paper and won II-Prize in EPISTEMICON-2K23 on 7th October 2023 organized by Dept of ECE ALIET with title “Miniaturization of circularly polarized crossed dipole antenna with high input impedance using AMC For Sub 6-GHz Applications.



- Mr.Sk. Nagoor Basha, Mr. M. Raghava, Mr.N. Sanjay of IV/IV B. Tech ECE presented a poster and won III-Prize in EPISTEMICON-2K23 on 7th October 2023 organized by Dept of ECE ALIET with a title “Design and Analysis of DRA antenna for 5G Communication Applications.

Department of ECE

Vision

To produce globally competitive and socially sensitized engineering graduates and to bring out quality research in the frontier areas of Electronics & Communication Engineering.

Mission

To provide quality and contemporary education in the domain of Electronics & Communication Engineering through periodically updated curriculum, best of breed laboratory facilities, collaborative ventures with the industries and effective teaching learning process.

To pursue research and new technologies in Electronics & Communication Engineering and related disciplines in order to serve the needs of the society, industry, government and scientific community.

PSOs

PSO1 Demonstrate proficiency in design, development and implementation of application oriented engineering systems using VLSI and IOT.

PSO2 Implement functional blocks of hardware / software designs for signal processing and communication applications.

POs

- PO1 → Engineering knowledge
- PO2 → Problem analysis
- PO3 → Design/development of solutions
- PO4 → Conduct investigations of complex problems
- PO5 → Modern tool usage
- PO6 → The engineer and society
- PO7 → Environment and sustainability
- PO8 → Ethics
- PO9 → Individual and team work
- PO10 → Communication
- PO11 → Project management and finance
- PO12 → Life-long learning

V R Siddhartha engineering College

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