



Courses ::

Advanced Digital Design using Verilog (Daily Batch)
Advanced Digital Design using Verilog (weekends only)
SystemVerilog + One Methodology
FPGA Porting, Debugging and Understanding
Live Projects (Bus Protocols, IP Cores etc)
Unix, Scripting and PERL

Training ::

Training on Advanced Digital Design using Verilog @ LUCID VLSI
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Projects ::

1. Design and Development of 5-Stage Processor
2. Design and Development of Dynamic scheduling Processor
3. Design and Development of Configurable Arbiter
4. Design and Development of ALU using Vedic multiplication & Vedic division
5. Design and Development of UART Controller using Synchronous FIFO (First In First Out)
6. Design and Development I2C Core using Synchronous FIFO:

About Raja Bandi:

Professional Summary





1. 1+ years of teaching experience at Lucid VLSI, Hyderabad
2. Two (2+) years of teaching experience at VedaIIT, Banjara Hills Hyderabad
3. Fifteen (15+) years of strong Industry experience in Design, Development and support at Synopsys, Qualcomm, RealChip, arm Ltd.

Which includes, four (4) years experience abroad, (At Synopsys Inc, California, USA)

Education

Pursuing Ph.D at IIIT, Gachibowli, Hyderabad in VLSI domain under Prof. R.Govindrajulu
University of Osmania, at CBIT (Chaitanya Bharathi Institute of Technology), Hyderabad
1992 : BE, (Electronics and Communication Engineering)
State Board of Technical Education and Training
1989: Diploma in Electronics from Govt., Institute of Electronics, Hyderabad

Experience

<p>Founder and Director of LIVE (Lucid Institute of VLSI Education)</p>	 <p>2012 onwa</p>
<p>Sr. Design Engineer at Veda IIT, Hyderabad - ~ 2+ years)</p>	 <p>2009- 2012</p>
<p>Manager CAE, Synopsys India Pvt Ltd, Hyderabad - ~ 3 years)</p>	 <p>2005-2008</p>
<p>Sr. Corporate Applications Engineer, Synopsys Inc, California, USA - ~4 years)</p>	 <p>2001-2005</p>
<p>Sr. Design Engineer, RealChip India Pvt Ltd, Chennai - ~2 Years)</p>	<p>1999-2001</p>

7. Design and Development of Configurable Multichannel Interrupt Controller	Sr. Member Technical Staff, at QualCore Logic Hyderabad – ~2 Years)	1997-1999
8. Design and Implementation of Digital Watch	Sr. Design Engineer at Advanced Radio Masts Ltd – ~4 Years)	1992-1996
9. Design and Implementaton of 4x4 Router (switch)		
10. Design and Implementation of Nikhilam Sutra (Vedic multiplication)		