

Dr.Pratikhya Raut



Designation: Assistant Professor

E-Mail ID : sweetpratikshya91@gmail.com

Contact at : EC-302, Department of Electronics and Communication

Engineering,

Velagapudi Ramakrishna Siddhartha Engineering College, Deemed to Be University, Kanuru, Vijayawada, Andhra Pradesh 520007.

Mobile: 8249387167

Email Id: drpratikhya@vrsiddhartha.ac.in

Education

Ph. D (Microelectronics and VLSI Design)

2023., VIT-APUniversity, Andhra Pradesh

M.Tech (VLSI and Embedded System Design)

2017, BPUT, Odisha

B.Tech (Electronics and Communication Engineering)

2014, BPUT, Odisha

Experience

Period	Designation	Institution / Organization
July 2023-Till	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering
Date		College
Mach 2019-	GIS Mapping Analyst	Cognizant Technology Solutions, Hyderabad
September		
2020		

Research Interests

Broad Area of Research:

VLSI Design, Semiconductor Device Modeling, Microelectronics, Junctionless FET, Tunnel FET, FinFET, Biosensors Design, Low Power Devices, TCAD Simulatio

Honors and Awards

- Received Best Research Award at University Level 2022-23 at VIT-AP University.
- Received Best Research Poster Presentation V-SAMSODH 2022 at VIT-AP University.

Courses Taught

- Basic Electronics
- Digital Design with FPGA
- Digital Circuit Design
- Semiconductor Device Physics
- CMOS VLSI Design
- AI Tools
- VLSI Design Lab
- Programmable Logic Devices Lab

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?hl=en&user=2PxNU9AAAAJ
Scopus	https://www.scopus.com/authid/detail.uri?authorId=57226812484
ORCID	https://orcid.org/0009-0004-8332-0604
Publons	https://www.webofscience.com/wos/author/record/KMA-1192-2024
Research Gate	https://www.researchgate.net/profile/Pratikhya-Raut
LinkedIn	https://www.linkedin.com/in/dr-pratikhya-raut-50ab61141/?originalSubdomain=in

Recognized Research Supervisor

S.No.	Department	University		

Funded Research Projects

Completed	Nil
On-going	Nil
Applied	01

Consultancy / Industry Projects

Completed	Nil
On-going	Nil
Applied	Nil

Patents

Granted	Nil
Published	Nil
Applied	Nil

Invited Talks

No. of Invited Talks	01

Professional Service / Reviewer

- Microsystem Technologies
- Silicon
- Discover Electronics
- ECS Journal of science & Technology
- Microelectronics Journal
- Semiconductor Science and Technologies
- IEEE sensors journal
- IEEE Access
- IEEE Transactions on Electron Devices

Professional Development Activities - Participations

Faculty Development and Training Programmes	05
(at least 5 Days)	
International / National Level Seminars	00 / 10
International / National Level Conferences	06 / 00
Workshops	12
Instructor Led / Self-Paced Courses	0
Webinars	5

Dr.Pratikhya Raut

Protikhya Raut
