

Dr. Gummadi Lakshmi Madhumati



Designation : Professor
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Velagapudi Ramakrishna Siddhartha Engineering College, Kanuru,
Vijayawada, Andhra Pradesh, 520007.

Education

Ph.D (Electronics and Communication Engineering)

2011, Jawaharlal Nehru Technological University Hyderabad, Kukatpally, Hyderabad

M.Tech (Digital Systems and Computer Electronics)

2003, Jawaharlal Nehru Technological University, Kukatpally, Hyderabad.

B.E(Electronics)

1991, Shivaji University, Kolhapur, Maharashtra

Experience

Period	Designation	Institution/Organization
February 2022 to Till date	Professor, Dept. of ECE	Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
2011-2022	Professor & Head, Dept. of ECE	Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada
2011-2011	Professor, Dept. of ECE	Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
2010-2011	Professor, Dept. of ECM	Prasad V Potluri Siddhartha Institute of Technology, Vijayawada
2007-2010	Associate Professor, Dept. of ECM	Prasad V Potluri Siddhartha Institute of Technology, Vijayawada
2005-2007	Lecturer, Dept. of ECE	Koneru Lakshmaiah College of Engineering, Greenfields, Vaddeswaram, Vijayawada
2002-2005	Lecturer, Dept. of ECE	Velagapudi Ramakrishna Siddhartha Engineering College, Vijayawada
1993 - 2001	Associative Lecturer, Dept. of ECE	V.K.R and V.N.B Polytechnic, Gudivada

Research Interests

Broad Area of Research:

- VLSI Design, Digital design using HDLs
- Physical design, Low Power VLSI Design
- DSP Architectures , Image processing Architectures and Bio Medical Applications
- Mixed signal VLSI design
- Wireless Communication
- Partial Reconfiguration, System on Chip, Hardware Accelerators

Courses Taught

Undergraduate Level

- Switching Theory & Logic Design
- Digital System Design & Digital IC Applications
- VLSI Design
- Digital Image Processing
- Computer Architecture and Organization
- Operating Systems
- Electronic Devices and Circuits
- Electronic Circuit Analysis
- Pulse & Digital Circuits
- Analog Communication
- Satellite Communication
- System Design Through Verilog

Postgraduate Level

- Digital System Design
- VLSI Technology and Design
- Digital Design using HDL
- CMOS Analog IC Design
- CMOS Digital IC design
- CPLD and FPGA Architectures and Applications
- Low Power VLSI Design
- CMOS Mixed Signal Circuit Design
- System on Chip Design
- Nano-electronics
- Hardware Software co-design
- System on Chip

Research Profile

Publications

The recent publications and research contributions can be viewed from the following URLs

Google Scholar	https://scholar.google.com/citations?hl=en&user=uUQDcu0AAAAI
Scopus	https://www.scopus.com/authid/detail.uri?authorId=35174862800
Orcid	https://orcid.org/0000-0002-1547-2583
LinkedIn	https://www.linkedin.com/in/gummadi-lakshmi-madhumati-7ba7a22b/

Recognized Research Supervisor

S.No.	Department	University
01.	Electronics and Communication Engineering	KL University, Greenfields, Vaddeswaram, Vijayawada
02.	Electronics and Communication Engineering	Jawaharlal Nehru Technological University, Kakinada
03.	Electronics and Communication Engineering	VR Siddhartha Engineering College, Deemed to be University, Vijayawada

Invited Talks

No. of Invited Talks 06

Professional Service/Reviewer

- Research Advisory Committee (RAC) member at Sardar Patel Institute of Technology, Andheri (W), Mumbai-58.
- Reviewer for IEEE conference on “IoT for Rural Healthcare 2021” (CIRH-2021) organized by VFSTR (Deemed to be University), Guntur, India.
- Reviewer for The Journal of Systems Architecture

Professional Development Activities-Participations

Faculty Development and Training Programmes (atleast 5Days)	8
International/National Level Conferences	12
Workshops	10
Instructor Led/Self-Paced Courses	1

Professional Bodies Membership Details

LMISTE Life Member of The Indian Society for Technical Education
LMISSE Indian Society of Systems for Science & Engineering (ISSE) Life Membership
IFERP Member of Institute for Engineering Research and Publication



(Dr.G.L.Madhumati)
