

# A.Srinivasa Chowdary



**Designation**: Assistant Professor

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**Contact at**: EC203, Department of Electronics and Communication

Engineering, Velagapudi Ramakrishna Siddhartha

Engineering College, Kanuru, Vijayawada, Andhra Pradesh

520007.

#### **Education**

M.Tech (Energy & Environment Technologies And Management)

2022, IIT Delhi

**B.Tech (Electronics and Communication Engineering)** 

2019, PVP Siddhartha Institute of Technology

### Experience

Period	Designation	Institution / Organization
2024-Till Date	Assistant Professor	Velagapudi Ramakrishna Siddhartha Engineering College

#### **Research Interests**

#### **Broad Area of Research:**

#### Honors and Awards

**NIL** 

### **Courses Taught**

- Advanced Digital Design Using Verilog (ADD Using Verilog).
- Workshop Practice Lab.

### **Research Profile**

### **Publications**

The recent publications and research contributions can be viewed from the following URLs

Google	https://scholar.google.com/citations?view_op=new_articles&hl=en&imq=Srinivasa+
Scholar	Chowdary+Appasani#
Research	https://www.researchgate.net/profile/Srinivasa-Chowdary
Gate	
Linked I	https://www.linkedin.com/in/srinivas97/

## Recognized Research Supervisor

S.No.	Department	University

## **Funded Research Projects**

Completed	Nil
On-going	Nil
Applied	Nil

## **Consultancy / Industry Projects**

Completed	Nil
On-going	Nil
Applied	Nil

### **Patents**

Granted	Nil
Published	Nil
Applied	Nil

## **Invited Talks**

No. of Invited Talks	Nil

### Professional Service / Reviewer

NIL

# **Professional Development Activities - Participations**

Faculty Development and Training Programmes	01
(at least 5 Days)	
International / National Level Seminars	0
International / National Level Conferences	0
Workshops	0
Instructor Led / Self-Paced Courses	0
Webinars	0

A. Srinivas (A.Srinivasa Chowdary)

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