

V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

Date: 16-07-2019

Ref: VRSEC/EIE/PAC/2019/13

Mr.P.Srinivas
Program Coordinator, EIE Dept., and
PAC Chairperson

To

The members of PAC-EIE

Sir/ Madam

Sub: Meeting of PAC-EIE on 17.07.2019- Reg.,

As the Program assessment Committee meeting of the EIE department is convened on 17.07.2019 at 4PM in Electronics and Instrumentation Engineering Department, you are requested to kindly make it convenient to attend the same.

Thanking you.

Yours sincerely,

P Srinivas
16/07/19

Agenda:

1. Attainment of COs, POs and PSOs in the academic year 2018-19 in module subjects.
2. Pass Percentage and targets for next academic year.
3. Any other related matter with the permission of the chair.

To

The Module coordinators

1. Electronics Module – Mr.V.N.Prudhvi Raj
2. Instrumentation Module – Dr.CSS Anupama
3. Control & Automation Module - Smt. A sumalatha

V. Nagendrakumar
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Sumalatha A
16/7/19