

**VR SIDDHARTHA ENGINEERING COLLEGE, VIJAYWADA**  
**DEPARTMENT OF ELECTRONICS AND INSTRUMENTATION ENGINEERING**

**Dt: 17/07/2019**

**Parent Meet**

A parent meet in EIE Department was conducted on 23/08/2015 in central auditorium at 10:00 A.M. 132 parents of II & III-year students attended the meeting and discussed about EIE curriculum, course structure, Course outcomes and programme outcomes, rules & regulations, infrastructure in the laboratories, intimation about attendance & marks and remedial classes for weak students with HOD, Programme Coordinator & concerned counselors. At the end of the parent meet the following suggestions are made by the parents.

1. Suggested to conduct parents meet once in a semester
2. Special Classes for Gate Exam
3. Campus recruitments in Core Sector should be improved.
4. Suggested to Conduct Personality & Skill development classes regularly
5. Suggested to have MOU's with Core sector to get industrial exposure to students
6. Intimation to parents about Academic Calendar & Time Table at the beginning of the semester
7. Intimation about student end semester result, Co & Extracurricular activities.
8. Update the website regularly.

*P. Srinivas*  
P.Srinivas 17/07/19  
Programme Coordinator

Dr.G.N.Swamy  
HOD: EIE



