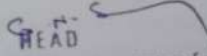


APSSDC - SIEMENS COE LABS VRSEC SCHEDULE

Acy 2019-20, Sem-1

S.No	MONTH	FROM	TO	Only one lab								labs can be operated simulta								
				Product Design and Mediate	Advanc ed Man ufacturi ng Lab	NC prog ramming /Machin e Lab	Test and Optimis ation La	Robotic s lab	Electrical & Energy E	Automati on Lab	Mechatro nics Lab	Process Instrume ntation Lab								
				30	30	24	24	24	24	24	24	24								
1	July	01-07-19	06-07-19	CITY DEVELOPMENT PROGR																
2		08-07-19	13-07-19																	
		15-07-19	20-07-19																	
3		22-07-19	27-07-19																	
4		29-07-19	03-08-19											3A	3A	3A				
5	August	05-08-19	10-08-19																	
6		12-08-19	17-08-19																	
7		19-08-19	24-08-19																	
8		26-08-19	31-08-19																	
9		02-09-19	07-09-19											3A	3A	3A				
10	September	09-09-19	14-09-19										3B	3B	3B					
11		16-09-19	21-09-19																	
12		23-09-19	28-09-19																	
13		30-09-19	05-10-19																	
14	October	07-10-19	12-10-19	D																
15		14-10-19	19-10-19																	
16		21-10-19	26-10-19																	
17		28-10-19	02-11-19	DULL HOLIDAYS																
18	November	04-11-19	09-11-19																	
19		11-11-19	16-11-19																	
20		18-11-19	23-11-19																	
21		25-11-19	30-11-19																	

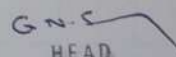

 HEAD
 Dept of Electronics & Instrumentation Engg
 V.R Siddhartha Engineering College
 VIJAYAWADA-520 007

APSSDC - SIES COE LABS VRSEC SCHEDULE

Acy. 2019-20 Sem-II

Only one lab can be operated simultaneously

S.No	MONTH	FROM	TO	Mid Exams / Training	Product Design and Validation Lab &	Advanced Manufacturing Lab	NC programming/Machines	Test and Optimisation	Robotics lab	Electrical & Energy Efficiency Lab	Automation Lab	Mechatronics Lab	Process Instrumentation Lab
No. of Students Required					30	30	24	24		24	24	24	24
1	November	04-11-19	09-11-19					pvpIII	SIP		SIP		
2		11-11-19	16-11-19		Dhanekula p	Dhanekula poly		pvpIII			SIP		
3		18-11-19	23-11-19								URCET-IV		
4		25-11-19	30-11-19								URCET-II	URCET-II	
5	December	02-12-19	07-12-19							2/4 sec A	2/4 sec A	2/4 sec A	
6		09-12-19	14-12-19		VR-3/4 sec	VR-3/4 sec				3/4 sec A	3/4 sec A	3/4 sec A	
7		16-12-19	21-12-19		VR-2/4 sec	VR-2/4 sec		VR-4/4	VR-				
8		23-12-19	28-12-19										
9	January	30-12-19	04-01-20		VR-3/4 sec	VR-3/4 sec					EIE -3B	EIE -3B	EIE -3B
10		06-01-20	11-01-20								3/4 sec B	3/4 sec B	3/4 sec B
11		13-01-20	18-01-20								2/4 sec B	2/4 sec B	2/4 sec B
12		20-01-20	25-01-20										
13	February	27-01-20	01-02-20										
14		03-02-20	08-02-20		VR-3/4 sec	VR-3/4 sec							
15		10-02-20	15-02-20										
16		17-02-20	22-02-20										
17	March	24-02-20	29-02-20										
18		02-03-20	07-03-20										
19		09-03-20	14-03-20		VR-2/4 sec	VR-2/4 sec		VR-4/4	VR-				
20		16-03-20	21-03-20		VR-2/4 sec	VR-2/4 sec		VR-4/4	VR-				
21		23-03-20	28-03-20										
		30-03-20	04-04-20										


 HEAD
 Dept of Electronics & Instrumentation Engg
 V R Siddhartha Engineering College
 VIJAYAWADA-520 007