

MEMORANDUM OF UNDERSTANDING

Between

**V.R.SIDDHARTHA ENGINEERING COLLEGE
VIJAYAWADA-520007**

and

**Panda Analog & Digital Systems
Vijayawada**

17th June 2020

Department of Electronics & Instrumentation Engineering

V.R.Siddhartha Engineering college(Autonomous)
(Approved by AICTE, Permitted by Govt.of A.P. and affiliated to JNTU)
Sponsored by Siddhartha Academy of General and Technical Education
Vijayawada, Andhra Pradesh-520007-10
Ph.No.0866 2582333
E-mail:www.hodeie@vrsiddhartha.ac.in

Panda Analog & Digital Systems

56-11-21, opp. J D Nagar Road,
Near Autonagar Road, Patamata
Vijayawada -520010
Ph No: 0866-2555097, 2544097
9848126025

MEMORANDUM OF UNDERSTANDING (MOU)

This MOU is between VR Siddhartha Engineering College & and M/s. Panda Analog & Digital Systems for continuous interaction between Industry and institution.

M/s. Panda Analog & Digital Systems is an Organization established by Sri P.Ravi kumar to develop Electrical and Electronic Automation systems.

VR Siddhartha Engineering College located at Vijayawada- 52007, Krishna District, A.P., India is approved by AICTE, New Delhi and Govt. of A.P. & affiliated to JNTU, Kakinada.

Terms & Conditions:**1. Nature of Relationship**

- 1.1 This MOU is for collaboration between both parties, for mutual benefit, for many purposes set out in Annexure – I to enhance quality of the educational experience for students of the college.
- 1.2 This MOU shall be valid for 3 years from the date and each party shall be at full liberty to terminate the collaboration with a notice period of 3 months.
- 1.3 Both parties shall take all reasonable steps to ensure successful completion of the collaboration and cooperate with each other in duly carrying out the obligation agreed upon.
- 1.4 The Institute will have to bear all expenses such as providing infrastructure, network and Internet access and other facilities required for the education and training.

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2. Mutual Obligation

- 2.1 This collaboration shall not be exclusive to both parties and shall not disallow each party from having similar collaboration with others. Except as expressly stated in this MOU, there shall be no obligation on any party to compensate the other in any manner.
- 2.2 Each party shall meet the expenses between them as mutually agreed.
- 2.3 Each party shall respect the other's intellectual property (IP) and shall not use any trade name, trade mark, symbol or designation belonging to the other, without prior written approval. No party shall hold out as an agent or representative of the other or create any liability for the other. The parties shall indemnify the other for breach of this clause.
- 2.4 Both parties shall maintain confidentiality about any information, plans, discussions, strategies or any material which shall be deemed to be confidential and marketed accordingly.

3. Limitations and Warranties

- 3.1 Each party shall ensure that the other is not put to any liability for any actions of the one party.
- 3.2 Each party represents that they have the full power and authority to enter into this MOU in general

4. General

- 4.1 Both parties will designate a representative from its side who will be the primary point of contact on behalf of that party.
- 4.2 Both parties shall not use the name of the other in any advertisement or make any public announcement without the prior written approval of the other.
- 4.3 Both parties will be regularly in touch with each other and explore setting up/ development of, related research projects in the college/industry.
- 4.4 Any dispute under this MOU will be settled in Vijayawada by mutual discussions.

In witness where of both the parties have set their hands to this MOU on this the 17th June 2020 at Vijayawada.

Department of Electronics & Instrumentation Engineering **Panda Analog & Digital Systems**

ANNEXURE-I

PURPOSE/SCOPE OF THE COLLABORATION:

1. M/s. Panda Analog & Digital Systems will permit a few batches of final year students of VR Siddhartha Engineering College to select and carry out the project work under the joint guidance.
2. M/s. Panda Analog & Digital Systems will from time to time participate/ render guidance in the activities of technical orientation conducted by VR Siddhartha Engineering College by way of Guest Lectures, Technical Seminars, Workshops and other events for the benefit of the faculty and students.
3. M/s. Panda Analog & Digital Systems will arrange to impart training to a mutually agreed number of students for a maximum period of 30 days, during May/June at their project sites or their associated companies.
4. Depute their executives to the college for campus interviews, depending upon the necessity.
5. Permit students of VR Siddhartha Engineering College for site visits for a day to any of their project sites.
6. VR Siddhartha Engineering College will depute their faculty and staff to Microsys Automation at their various projects for getting them acquainted for latest trends of technology.
7. VR Siddhartha Engineering College will give lectures to company employs in the relevant subjects of engineering and extend the services of their library, laboratories and workshop facilities for joint research and development on various technologies.
8. VR Siddhartha Engineering College will also from time to time participate / render guidance for activities of technical orientation and interviews conducted by Microsys Automation by way of guest lecturers etc.

For Panda Analog & Digital Systems



Proprietrix

M/s. Panda Analog & Digital Systems
Vijayawada.



For VR Siddhartha Engineering College
Vijayawada

HEAD

Dept. of Electronics & Instrumentation Engg
V R Siddhartha Engineering College
VIJAYAWADA-520 007