

V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

Dt.:29.12.20

Subject Code/Name: 17EI3502 Digital Signal Processing

Class: III Year, V Semester

S.No.	Regd. Numbers	Name	Section (A/B)	Marks obtained
1	178W1A1003	Aluri Uday Teja	A	ABSENT
2	178W1A1046	Sathi Venkatesh	A	4
3	188W1A1003	Bacchu Sai Sahithi	A	4
4	188W1A1007	Bommireddi Prajaktha Sekhar	A	4
5	188W1A1010	Dakarapu Sampath	A	2
6	188W1A1011	Dara Suryanadh	A	4
7	188W1A1013	Gorrepati Prabhath	A	3
8	188W1A1018	Kannasani Sai Kumar	A	2
9	188W1A1019	Katta Pavani	A	4
10	188W1A1022	Kommineni Upendra Sai	A	4
11	188W1A1024	Kuchipudi Ramya	A	3
12	188W1A1026	Kunapareddy Saroja	A	4
13	188W1A1028	Mathangi Bala Pavan	A	4
14	188W1A1031	Myneni Charishma	A	4
15	188W1A1032	Pabbiseti Durga Prasad	A	4
16	188W1A1034	Panchakarla Anjali	A	4
17	188W1A1035	Pasam Nirmal Kumar	A	4
18	188W1A1036	Pathuri Gopi	A	2
19	188W1A1038	Ravipati Mounika	A	4
20	188W1A1040	Saranam Hannsika	A	4

21	188W1A1042	Seera Gopal Rao	A	3
22	188W1A1043	Shaik Afreed	A	2
23	188W1A1049	Yannam Jagadeeshwara Phani	A	3
24	198W5A1005	Dondapati Ajeshbabu	A	3
25	198W5A1007	Gudivada Soma Sekhar	A	3
26	198W5A1008	Kadim Rama Mani Venkata Ganesh	A	2
27	198W5A1012	Reddy Praneeth	A	4
28	198W5A1013	Satti Mahesh Reddy	A	4

Academic Year: 2020-21

Faculty Name: Dr. S. Srinivasulu Raju, Assist. Professor

Slow learners list based on A-I (Who got less than 5 Marks)


(S SRINIVASULU RAJU)

**V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

Dt.:29.12.20

QUALITY CIRCLES (Remedial classes based on A-I marks)

Name of the QC group: EIE quality circle – III Year, V Semester

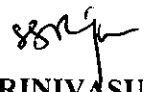
Subject: 17EI3502 Digital Signal Processing

Academic Year: 2020-21

Faculty Name: Dr. S. Srinivasulu Raju, Assist. Professor

S. No	QC leaders	Potential Members	Meeting schedule	Topics covered
1	188W1A1008	178W1A1003	A-Section Dates: 12.12.20, 13.12.20 & 15.12.20 Time: 3.00PM - 4.00PM	Date : 12.12.20 • Properties of Fourier Transform and Example problems. Date : 13.12.20 • Problems on linear convolution. Date : 15.12.20 • Explanation about classifications of systems. And example problems.
		178W1A1046		
		188W1A1003		
2	188W1A1001	188W1A1007		
		188W1A1010		
		188W1A1011		
3	188W1A1002	188W1A1013		
		188W1A1018		
		188W1A1019		
4	188W1A1006	188W1A1022		
		188W1A1024		
		188W1A1026		
5	188W1A1015	188W1A1028		
		188W1A1031		
		188W1A1032		
6	188W1A1039	188W1A1034		
		188W1A1035		
		188W1A1036		
7	188W1A1045	188W1A1038		
		188W1A1040		
		188W1A1042		
8	188W1A1048	188W1A1043		
		188W1A1049		
		198W5A1005		
9	198W5A1009	198W5A1007		

		198W5A1008		
		198W5A1012		
		198W5A1013		


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V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

Dt.:29.12.20

Remedial classes (Quality circles) Attendance statement of slow learners based on
A-I Marks

S. No.	Regd. Numbers	Section (A)	Marks obtained	Student Signature		
				12.12.20	13.12.20	15.12.20
1	178W1A1003	A	ABSENT	A. Uday Teja	A. Uday Teja	A. Uday Teja
2	178W1A1046	A	4	Hrishi	Hrishi	Hrishi
3	188W1A1003	A	4	B. Sai Sahith	B. Sai Sahith	B. Sai Sahith
4	188W1A1007	A	4	B. Prajaktha	B. Prajaktha	B. Prajaktha
5	188W1A1010	A	2	D. Sampath	D. Sampath	D. Sampath
6	188W1A1011	A	4	D. Praveen	D. Praveen	D. Praveen
7	188W1A1013	A	3	G. Prabhath	G. Prabhath	G. Prabhath
8	188W1A1018	A	2	Jee	Jee	Jee
9	188W1A1019	A	4	Pavani	Pavani	Pavani

10	188W1A1022	A	4	K. Upendra Sai	K. Upendra Sai	K. Upendra Sai
11	188W1A1024	A	3	K. Ramya	K. Ramya	K. Ramya
12	188W1A1026	A	4	Saroja k	Saroja k	Saroja k
13	188W1A1028	A	4	Balapavan	Balapavan	Balapavan
14	188W1A1031	A	4	Chase	Chase	Chase
15	188W1A1032	A	4	P.D	P.D	P.D
16	188W1A1034	A	4	P. Anjali	P. Anjali	P. Anjali
17	188W1A1035	A	4	nirmal	nirmal	nirmal
18	188W1A1036	A	2	P. Gopi	P. Gopi	P. Gopi
19	188W1A1038	A	4	R. Mounika	R. Mounika	R. Mounika
20	188W1A1040	A	4	S. Hanu	S. Hanu	S. Hanu
21	188W1A1042	A	3	S. Gopal	S. Gopal	S. Gopal
22	188W1A1043	A	2	Shree	Shree	Shree
23	188W1A1049	A	3	Y. Jeshu	Y. Jeshu	Y. Jeshu
24	198W5A1005	A	3	D. Jeeva	D. Jeeva	D. Jeeva
25	198W5A1007	A	3	G. Suresh	G. Suresh	G. Suresh
26	198W5A1008	A	2	K. Ganes	K. Ganes	K. Ganes
27	198W5A1012	A	4	R. Praneeth	R. Praneeth	R. Praneeth
28	198W5A1013	A	4	S. Nela	S. Nela	S. Nela


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**V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

Dt.:29.12.20

Outcome of quality circles in I-Sessional examination of 17EI3502

S. No.	Regd. Numbers	Section (A/B)	Marks obtained In A-I	Marks obtained in S-I	Outcome of quality circles
1	178W1A1003	A	ABSENT	1	Scored below 50% marks
2	178W1A1046	A	4	4.5	Scored below 50% marks
3	188W1A1003	A	4	11	Scored above 50% marks
4	188W1A1007	A	4	2	Scored below 50% marks
5	188W1A1010	A	2	1	Scored below 50% marks
6	188W1A1011	A	4	7	Scored above 50% marks
7	188W1A1013	A	3	3	Scored below 50% marks
8	188W1A1018	A	2	5	Scored below 50% marks
9	188W1A1019	A	4	9	Scored above 50% marks
10	188W1A1022	A	4	5	Scored below 50% marks
11	188W1A1024	A	3	3	Scored below 50% marks
12	188W1A1026	A	4	6	Scored equal 50% marks
13	188W1A1028	A	4	3	Scored below 50% marks
14	188W1A1031	A	4	2	Scored below 50% marks
15	188W1A1032	A	4	5	Scored below 50% marks
16	188W1A1034	A	4	12	Scored above 50% marks
17	188W1A1035	A	4	7	Scored above 50% marks
18	188W1A1036	A	2	6	Scored equal 50% marks

19	188W1A1038	A	4	10	Scored above 50% marks
20	188W1A1040	A	4	7	Scored above 50% marks
21	188W1A1042	A	3	3	Scored below 50% marks
22	188W1A1043	A	2	6	Scored equal 50% marks
23	188W1A1049	A	3	6	Scored equal 50% marks
24	198W5A1005	A	3	7	Scored above 50% marks
25	198W5A1007	A	3	4	Scored below 50% marks
26	198W5A1008	A	2	6	Scored equal 50% marks
27	198W5A1012	A	4	7	Scored above 50% marks
28	198W5A1013	A	4	4	Scored below 50% marks

Remarks:

1. Out of 28 students, 14 students secured above or equal to 50% marks in I Sessional examination due to quality circle methodology.
2. Based on the outcome of quality circles methodology, the same methodology is adopted for slow learners in I Sessional examination with modifications in student mentors.


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**V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

Dt.:12.02.21

Subject Code/Name: 17EI3502 Digital Signal Processing

Class: III Year, V Semester

Academic Year: 2020-21

Faculty Name: Dr. S. Srinivasulu Raju, Assist. Professor

Slow learners list based on S-I (Who got less than 6 Marks)

S. No.	Regd. Numbers	Name	Section (A/B)	Marks obtained In S-I
1	168W1A1022	Koka Sri Jagadish	A	4
2	178W1A1003	Aluri Uday Teja	A	1
3	178W1A1006	Burugupalli Sai Sandeep	A	4
4	178W1A1039	Parasa Akhil	A	1
5	178W1A1046	Sathi Venkatesh	A	4.5
6	178W1A1051	Umamaheswara Rao Bathina	A	1
7	188W1A1006	Bhogadula Lohith Sai Vishnu	A	4
8	188W1A1007	Bommireddi Prajaktha Sekhar	A	2
9	188W1A1010	Dakarapu Sampath	A	1
10	188W1A1013	Gorrepati Prabhath	A	3
11	188W1A1018	Kannasani Sai Kumar	A	5
12	188W1A1020	Kodali Bhavya Annapurna	A	5
13	188W1A1022	Kommineni Upendra Sai	A	5
14	188W1A1023	Konakalla Venkata Naga Hyndavi	A	3

15	188W1A1024	Kuchipudi Ramya	A	3
16	188W1A1025	Kudaravalli Dheeraj	A	2
17	188W1A1027	Maddineni Yashesvi	A	5
18	188W1A1028	Mathangi Bala Pavan	A	3
19	188W1A1030	Muthyalapati Shasi Kumar	A	2
20	188W1A1031	Myneni Charishma	A	2
21	188W1A1032	Pabbiseti Durga Prasad	A	5
22	188W1A1042	Seera Gopal Rao	A	3
23	198W5A1002	Adavikolanu Durga Prasad	A	4.5
24	198W5A1003	Akulapavankumar	A	4
25	198W5A1006	Ghanta Hemanth Venkata Kanaka Sai Ayyappa	A	4
26	198W5A1007	Gudivada Soma Sekhar	A	4
27	198W5A1010	Pathan Ashwak Khan	A	5
28	198W5A1011	Pokala Rangarao	A	5
29	198W5A1013	Satti Mahesh Reddy	A	4

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**V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

Dt.:12.2.21

QUALITY CIRCLES (Remedial classes based on S-I marks)

Name of the QC group: EIE quality circle – III Year, V Semester

Subject: 17EI3502 Digital Signal Processing

Academic Year: 2020-21

Faculty Name: Dr. S. Srinivasulu Raju, Assist. Professor

S.No	QC leaders	Potential Members	Meeting schedule	Topics covered
1	188W1A1002	168W1A1022	A-Section Dates: 20.01.21, 21.01.21 & 22.01.21 Time: 2.00PM - 3.00PM	Date : 20.01.21 • Problems on DIT-FFT. Date : 21.01.21 • Problems on circular convolution. Date : 22.01.21 • Problems on DIF-FFT.
		178W1A1003		
		178W1A1006		
2	188W1A1008	178W1A1039		
		178W1A1046		
		178W1A1051		
3	188W1A1001	188W1A1006		
		188W1A1007		
4	188W1A1003	188W1A1010		
		188W1A1013		
		188W1A1018		
5	188W1A1015	188W1A1020		
		188W1A1022		
		188W1A1023		
6	188W1A1035	188W1A1024		

		188W1A1025		
		188W1A1027		
7	188W1A1038	188W1A1028		
		188W1A1030		
		188W1A1031		
8	188W1A1040	188W1A1032		
		188W1A1042		
		198W5A1002		
9	188W1A1045	198W5A1003		
		198W5A1006		
		198W5A1007		
10	198W5A1009	198W5A1010		
		198W5A1011		
		198W5A1013		

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V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

Dt.:12.2.21

Remedial classes (Quality circles) Attendance statement of slow learners based on
S-I Marks

S. No.	Regd. Numbers	Section (A)	Marks obtained	Student Signature		
				20.01.21	21.01.21	22.01.21
1	168W1A1022	A	4	Jagadeesh	Jagadeesh	Jagadeesh
2	178W1A1003	A	1	A. Uday Teja	A. Uday Teja	A. Uday Teja
3	178W1A1006	A	4	Bhikshu	Bhikshu	Bhikshu
4	178W1A1039	A	1	P. Akhil	P. Akhil	P. Akhil
5	178W1A1046	A	4.5	Bhikshu	Bhikshu	Bhikshu
6	178W1A1051	A	1	B. Uma Maheswarana	B. Uma Maheswarana	B. Uma Maheswarana
7	188W1A1006	A	4	B. Lakshmi	B. Lakshmi	B. Lakshmi
8	188W1A1007	A	2	B. Pranjitha	B. Pranjitha	B. Pranjitha
9	188W1A1010	A	1	D. Pranjitha	D. Pranjitha	D. Pranjitha
10	188W1A1013	A	3	G. Prabath	G. Prabath	G. Prabath
11	188W1A1018	A	5	Sai	Sai	Sai
12	188W1A1020	A	5	K. Bhanga	K. Bhanga	K. Bhanga
13	188W1A1022	A	5	K. Upendra Sai	K. Upendra Sai	K. Upendra Sai
14	188W1A1023	A	3	K. V. N. Thyra	K. V. N. Thyra	K. V. N. Thyra
15	188W1A1024	A	3	K. Ramya	K. Ramya	K. Ramya
16	188W1A1025	A	2	Deepthi	Deepthi	Deepthi
17	188W1A1027	A	5	M. Vashe	M. Vashe	M. Vashe
18	188W1A1028	A	3	Balapravan	Balapravan	Balapravan

19	188W1A1030	A	2	Mohgi.	Mye	Men
20	188W1A1031	A	2	class	class	class
21	188W1A1032	A	5	P. Rao	P. Rao	P. Rao
22	188W1A1042	A	3	S. Gopal	S. Gopal	S. Gopal
23	198W5A1002	A	4.5	A.D.P	A.D.P	A.D.P
24	198W5A1003	A	4	A.P.K	A.P.K	A.P.K
25	198W5A1006	A	4	G. Ayyappa	G. Ayyappa	G. Ayyappa
26	198W5A1007	A	4	G. Suresh	G. Suresh	G. Suresh
27	198W5A1010	A	5	P. Suresh	P. Suresh	P. Suresh
28	198W5A1011	A	5	P. Rangar	P. Rangar	P. Rangar
29	198W5A1013	A	4	S. Mohan	S. Mohan	S. Mohan


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**V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

Dt.:12.02.21


Outcome of quality circles in II-Assignment examination of 17EI3502

S. No.	Regd. Numbers	Section (A/B)	Marks obtained In S-I	Marks obtained in A-II	Outcome of quality circles
1	168W1A1022	A	4	5	Scored equal 50% marks
2	178W1A1003	A	1	4	Scored below 50% marks
3	178W1A1006	A	4	6	Scored above 50% marks
4	178W1A1039	A	1	3	Scored below 50% marks
5	178W1A1046	A	4.5	5.5	Scored above 50% marks
6	178W1A1051	A	1	5	Scored equal 50% marks
7	188W1A1006	A	4	3	Scored below 50% marks
8	188W1A1007	A	2	2	Scored below 50% marks
9	188W1A1010	A	1	1	Scored below 50% marks
10	188W1A1013	A	3	5	Scored equal 50% marks
11	188W1A1018	A	5	6	Scored above 50% marks
12	188W1A1020	A	5	3	Scored below 50% marks
13	188W1A1022	A	5	9	Scored above 50% marks
14	188W1A1023	A	3	6	Scored above 50% marks
15	188W1A1024	A	3	5	Scored equal 50% marks
16	188W1A1025	A	2	1	Scored below 50% marks
17	188W1A1027	A	5	4	Scored below 50% marks
18	188W1A1028	A	3	7	Scored above 50% marks

19	188W1A1030	A	2	2	Scored below 50% marks
20	188W1A1031	A	2	4	Scored below 50% marks
21	188W1A1032	A	5	4	Scored below 50% marks
22	188W1A1042	A	3	7	Scored above 50% marks
23	198W5A1002	A	4.5	6	Scored above 50% marks
24	198W5A1003	A	4	6	Scored above 50% marks
25	198W5A1006	A	4	3	Scored below 50% marks
26	198W5A1007	A	4	3	Scored below 50% marks
27	198W5A1010	A	5	4	Scored below 50% marks
28	198W5A1011	A	5	6	Scored above 50% marks
29	198W5A1013	A	4	4	Scored below 50% marks

Remarks:

1. Out of 29 students, 14 students secured above or equal to 50% marks in II Assignment examination due to quality circle methodology.
2. Based on the outcome of quality circles methodology, the same methodology is adopted for slow learners in II Assignment examination with modifications in student mentors.


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**V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA – 7(Autonomous)
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

Dt.:22.04.21

III/IV B.Tech semester Academic Year: 2019-20

**Final outcome of quality circles in semester end examination of 17EI3502 Digital Signal
Processing**

1. In I Assignment examination, number of slow learners who got less than 50% marks are **10**. Out of **10** students, **6** students secured above or equal to 50% marks in I sessional examination due to quality circle methodology. Four students were absent for examination due to health problems.
2. In I sessional examination, number of slow learners who got less than 50% marks are **9**. Out of **9** students, **4** students secured above or equal to 50% marks in II Assignment examination due to quality circle methodology. One student was absent for examination due to health problems.
3. The number of slow learners are reduced from **10** to **4** after I sessional examination due to this methodology.
4. In II Assignment examination, number of slow learners who got less than 50% marks are **23**. Out of **23** students, **11** students secured above or equal to 50% marks in II sessional examination due to quality circle methodology.
5. The number of slow learners is reduced from **23** to **12** after II assignment examination due to this methodology.
6. In the semester end examination, out of **65** students, **56** students were passed and only **9** students were failed. This shows that there is an improvement (**86.2%**) in pass percentage due to quality circle methodology.
7. Based on the final outcome of quality circles methodology, the same methodology can be adapted for slow learners in this course when offered.


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