

V.R.SIDDHARTHA ENGINEERING COLLEGE: VIJAYAWADA - 7
DEPARTMENT OF ELECTRONICS&INSTRUMENTATION ENGINEERING
Academic Year 2018- 2019 (I Semester)

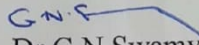
Class: II / IV EIE SECTION - A ROOM NO: EI 447 w.e.f: 02-07-2018

TIME TABLE

| Day | 8:40-9:40 | 9:40-10:40 | 10:40-11:40 | 11:40-12:40 | 12:40-1:40 | 1:40-2:40 | 2:40-3:40 | 3:40-4:40 | |
|-----|-----------|------------------|-------------|-----------------------|--|-----------|-----------|-----------|----------|
| Mon | 17EI3303 | ←---17HS1353---→ | | L U N C H | 17EI3302 | 17HS1306 | NPTEL | 17MC1307 | |
| Tue | 17MA1301 | 17EI3303 | Couns | | ←-----17EI3351/3352-----→ Room No EI 427, EI428 | | | | 17MC1307 |
| Wed | 17EI3302 | 17MA1301 | 17EI3304 | | ←--Remedial classes--→ | | | | |
| Thu | 17EI3304 | 17EI3303 | 17MA1301 | | ←-----17EI3351/3352-----→ Room No EI 427, EI428 | | | | 17HS2305 |
| Fri | 17EI3303 | 17EI3302 | 17EI3304 | | 17HS1306 | 17MA1301 | | 17HS2305 | |
| Sat | 17MA1301 | 17EI3302 | NPTEL | 17HS2305 | | | | | |

| | | |
|-------------|--|---|
| 17MA1301 | Complex Analysis & Numerical Methods (3+1) | : D.Rajani (Maths Dept.) |
| 17EI3302 | Network Theory (3+1) | : T.Suneel (EEE Dept.) |
| 17EI3303 | Analog Electronic Circuits (3+1) | : Dr.G.N.Swamy |
| 17EI3304 | Sensors & Transducers(3) | : Dr.CSS Anupama |
| 17HS2305 | Humanities Elective(1) | : Mr.Jamindhar/Dr.I.Ravi |
| 17HS1306 | Logic and Reasoning (2) | : P.Suneetha(T&P Dept) |
| 17MA1301(T) | Complex Analysis & Numerical Methods | : D.Rajani (Maths Dept.) |
| 17EI3302(T) | Network Theory | : T.Suneel (EEE Dept.) |
| 17EI3351 | Electronic Circuits Lab (3) | : Ch.Jaya Lakshmi/P.Sushma Chowdary |
| 17EI3352 | Transducers Lab (3) | : K.Prasanti/ D.Divya Bharati |
| 17HS1353 | Communication skills Lab(2) | :Dr.B.V.V.Padmavathi/ Dr.K.Ramchandra (English Dept) |
| 17MC1307 | Indian constitution | :Ch.Sreenivasa rao |
| RC | Remedial Class | |

Class Counsellor : 1. V.Naga Prudhvi Raj
 2. M.Srinivas
 3. Md. Firose Shaik


 Dr.G.N.Swamy

PROF.& HEAD::EIE

HEAD
 Dept. of Electronics & Instrumentation Engg
 V.R. Siddhartha Engineering College
 VIJAYAWADA-520 007

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DEPARTMENT OF ELECTRONICS&INSTRUMENTATION ENGINEERING
Academic Year 2018 – 2019 (I Semester)

Class: II / IV EIE SECTION - B ROOM NO: EI 448 w.e.f: 02-07-2018

TIME TABLE

| Day | 8:40-9:40 | 9:40-10:40 | 10:40-11:40 | 11:40-12:40 | 12:40-1:40 | 1:40-2:40 | 2:40-3:40 | 3:40-4:40 |
|-----|--|------------------|-------------|-----------------------|--|-----------|-----------|------------|
| Mon | 17MA1301 | 17EI3304 | 17EI3302 | L U N C H | ←--Remedial classes--→ | | | |
| Tue | 17EI3303 | ←---17HS1353---→ | | | 17EI3302 | 17HS1306 | 17MC1307 | 17MC1307 |
| Wed | 17EI3304 | 17MA1301 | 17EI3303 | | ←-----17EI3351/3352-----→ Room No : EI 427, EI428 | | | |
| Thu | 17EI3302 | 17EI3303 | 17EI3304 | | | | | 17HS2305 |
| Fri | ←-----17EI3351/3352-----→ Room No : EI 427, EI428 | | | | 17HS1306 | Couns | 17MA1301 | 17 HS 2305 |
| Sat | 17MA1301 | 17EI3303 | 17EI3302 | | 17HS2305 | | | |

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|-------------|--|---|
| 17MA1301 | Complex Analysis & Numerical Methods (3+1) | : M.Venkat Rao(Maths Dept.) |
| 17EI3302 | Network Theory (3+1) | : T.Suneel (EEE Dept.) |
| 17EI3303 | Analog Electronic Circuits (3+1) | : Ch.Jaya Lakshmi |
| 17EI3304 | Sensors & Transducers(3) | : Dr.CSS Anupama |
| 17HS2305 | Humanities Elective(1) | : Mr.Jamindhar/Dr.I.Ravi |
| 17HS1306 | Logic and Reasoning (2) | : N.Durgaprasad(T&P Dept) |
| 17MA1301(T) | Complex Analysis & Numerical Methods | : M.Venkat Rao (Maths Dept.) |
| 17EI3302(T) | Network Theory | : T.Suneel (EEE Dept.) |
| 17EI3351 | Electronic Circuits Lab (3) | : Ch.Jaya Lakshmi/P.Sushma Chowdary |
| 17EI3352 | Transducers Lab (3) | : G.Jalalu/ D.Divya Bharati |
| 17HS1353 | Communication skills Lab(2) | : P.Venkata Ramana/ Dr.K.Ramchandra (English Dept) |
| 17MC1307 | Indian constitution | :Ch.Sreenivasa rao |
| RC | Remedial Class | |

Class Counsellor : 1. Dr.CSS Anupama
2. K.Vijaya Lakshmi
3. Ch.Jaya Lakshmi

G.N.S
Dr.G.N.Swamy

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