

02-12-2017

Minutes of board of studies meeting, held on 02-12-2017 at 10:00 am in the chambers of HoD, EIE.

Agenda:

1. To review & approve the scheme of instruction & examination of B.Tech EIE program - VR17 Regulation
2. To discuss & approve the proposed syllabus for the second year as per scheme of instruction.
3. To discuss & finalize transitory regulations for VR14 regulation batch students.
4. Ratification of exempted & additional subjects for the student Raupelli Anani transferred from VNRVJIT, Hyderabad to URSEC, Vijayawada.
5. Any other matter with the permission of chair.

minutes

The following recommendations are made:

- 1) Restructure the syllabus of Digital electronics
- 2) Reduce the syllabus in the subject Electrical & Electronic measurement.
- 3) Suggested to reduce the number of subjects in VR17 curriculum.
- 4) With the above modifications, VR17 scheme of instructions of second year syllabus is approved.
- 5) Transitory Regulations for VR14 regulation batch students is approved.
- 6) Exempted subjects, for Raupelli Anani, transferred from VNRVJIT to URSEC, is approved.
- 7) PEO's are approved

Name of the Bos member	Designation & Affiliation	Signature
1) Dr. G. N. Suresh	Prof & HoD EIE, URSEC	G. N. Suresh
2) Dr. A. Bhujanga Rao	Prof, Subject Expert Instrument Technology, AU	A. Bhujanga Rao
3) Dr. V. Narayan Kumar	Sr. Ass. Prof, R&A School of Electronics, VITU, Vellore	V. Narayan Kumar
4) B. Samba Siva Rao	Managing Director, Rep Industries Microsys Automation Ltd. Hyd	B. Samba Siva Rao
5) J. Nityanandam	Assoc Prof EIE URSEC Vij.	J. Nityanandam
6) P. Srinivas	Assoc Prof EIE, URSEC Vij.	P. Srinivas
7) C. S. S. Anupama	Assoc Prof EIE URSEC Vij.	C. S. S. Anupama
8) Dr. H. K. Prith	EIE URSEC Vij Assoc Prof	Absent
9) V. N. Prudh. Raj	EIE URSEC Vij Assoc Prof	V. N. Prudh. Raj
10) A. Sumanlatha	EIE URSEC Vij Assoc Prof	A. Sumanlatha
11) K. Vijaya Lakshmi	EIE URSEC Vij Asst Prof	K. Vijaya Lakshmi
12)	EIE URSEC Vij	