



V R SIDDHARTHAENGINEERING COLLEGE: VIJAYAWADA
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
Academic Year: 2022-23

Stakeholder	Feedback Collected	Action Taken 2022-2023
Students	Include more job oriented or skill oriented courses in the syllabus	Digital System Design with FPGA and Real Time Operating Systems courses is introduced in VI & VII semester of VR 20 curriculum.
Alumni	Provide industry ready certification courses.	PEGA training is provided to make students industry ready
Employer	Include industry personals in delivering core subject syllabus.	Services of industry persons are being used in delivering Industrial 4.0 course to final year students
Parents	Conduct mock interviews to make the students ready for job interviews.	In T&P classes, number of mock tests and mock interviews are conducted
Teachers	Motivate students to do quality projects for solving real time industrial or societal problems	Publication of project outcomes in IEEE conferences or Scopus indexed journals is made mandatory


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