

V.R.SIDDHARTHA ENGINEERING COLLEGE, VIJAYAWADA

Department of Electronics & Instrumentation Engineering

Academic Year	Stake Holder	Feedback / Suggestions	Action taken / Resolutions / Approvals
2023-24	Students	To provide more practical knowledge to students to implement good projects and work with real life applications.	➤ Programming labs are increased and more number of projects is included in the scheme of instructions of VR20 Regulations.
		Suggested to have student clubs and encourage student talent in curricular and extracurricular activities.	➤ Spandana club is initiated in the department to conduct number of curricular and extracurricular events.
	Parents	To provide internship with stipend for the students.	➤ Internships for the students is made mandatory as part of the curriculum. ➤ MOUs are made with various industries.
	Alumni	To train the students rigorously in view of placements.	➤ Training and Placement classes are included in the time table from third semester onwards. ➤ Experienced resource persons from outside are invited to facilitate training to the students
	Industry	To introduce advanced courses in the curriculum to meet the industry needs	➤ Advanced courses like Digital System Design with FPFA, Drives and Control for Industrial Automation are offered to the students.
	Faculty/ Subject Expert	To refine the course outcomes of the courses in VR 23 curriculum.	➤ Course outcomes are improvised based on the guidelines of OBE
		To publish the students projects in conferences or journals.	➤ It was made mandatory for the students to publish the major project implementation in either conferences or journals which are in Scopus database.