

V R SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA – 520 007

Date: 19/08/2024

B.TECH MINORS TIME TABLE FOR B.TECH., IV-SEMESTER (VR20)
EXAMINATIONS, AUGUST-2024 (2022 BATCH)

Time: 2.00 PM to 5.00 PM

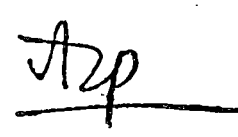
DATE	CE / ECE / EIE	CE / ECE / EEE / EIE / ME
31/08/2024 Saturday	20AI&DSM4701 – Introduction to Artificial Intelligence and Data Science	20CSM4701 - Data Structures & Algorithms

B.TECH HONORS TIME TABLE FOR B.TECH., IV-SEMESTER (VR20)
EXAMINATIONS, AUGUST-2024 (2022 BATCH)

Time: 2.00 PM to 5.00 PM

DATE	CE	CSE (AI&ML)	AI&DS / CSE	ECE	IT
31/08/2024 Saturday	20CEH4801B – Sustainable Construction Methods	20AI&MLH4801 – Mathematical Models for Machine Learning	20CSH4801A – Advanced Python Programming 20AI&DSH4801B / 20CSH4801E – Front End Technologies	20ECH4801 – Programming Languages for Embedded Software	20ITH4801A – Data Analytics 20ITH4801B – Introduction to Security: Cyberspace, Cybercrime and Cyber security


(Dr. T S R CHOWDARY)
Dy. Controller of Examinations


(Dr. A V RATNA PRASAD)
Principal