

**V R SIDDHARTHA ENGINEERING COLLEGE**  
(AUTONOMOUS)  
VIJAYAWADA

Date: 28/11/2024


**NOTICE**

The Semester-end Examination of B.Tech., VII-Semester (VR20) Regular & Supplementary and B.Tech VII-Semester CBCS Examinations for the following subjects is rescheduled as given below due to placements.

**B.Tech., VII-Semester Regular, Supplementary and CBCS Exams**

Subject Code & Name	Actual Schedule of Examination & Date & Time	Postponement of Examination & Date & Time
20EI7301 – Computer Control of Processes <b>Regular, Supplementary &amp; CBCS</b>	13/12/2024 Friday (2.00PM to 5.00PM)	16/12/2024 Monday (2.00PM to 5.00PM)
20CS7402A – Business Intelligence and Data Visualization 20CS7402D – UI & UX Design	27/11/2024 Wednesday (2.00PM to 5.00PM)	16/12/2024 Monday (2.00PM to 5.00PM)

Students are advised to note the change in the time-table and attend the semester end examinations accordingly.

  
(Dr. T S R CHOWDARY)  
Controller of Examinations

To  
All HODs  
Notice Board  
Web site  
Exam Section

  
(Dr. A V RATNA PRASAD)  
Principal

