

**V R SIDDHARTHA ENGINEERING COLLEGE**  
**(AUTONOMOUS)**  
**VIJAYAWADA – 520 007**

Date: 20/12/2022

**CBCS TIME TABLE FOR B.TECH., V-SEMESTER (VR20) STUDENTS APPEARING FOR VI-SEMESTER (VR20) EXAMINATIONS, DECEMBER-2022**


Time: 9.00 AM to 12.00 Noon

DATE	IT
<b>02/01/2023</b> <b>Monday</b>	20IT6404A – Data Visualization 20IT6404B – Big Data
<b>04/01/2023</b> <b>Wednesday</b>	20IT6205A – Agile Software Development 20IT6205B – Automata & Compiler Design

**CBCS TIME TABLE FOR B.TECH., V-SEMESTER (VR20) STUDENTS APPEARING FOR VII-SEMESTER (VR20) EXAMINATIONS, DECEMBER-2022**

Time: 9.00 AM to 12.00 Noon

DATE	EIE
<b>02/01/2023</b> <b>Monday</b>	20EI7301 – Computer Control of Processes

  
**(Dr. T S R CHOWDARY)**  
**Dy. Controller of Examinations**

  
**(Dr. A V RATNA PRASAD)**  
**Principal**