REGISTRATION FORM

Name: ....

Designation: ....

Institution/Organization: ....

....

Address: ....

Qualifications: ....

Experience in years: Teaching: ....

Research: ....

Sex: Male / Female / Others

Telephone No. ....

Email: ....

Signature of the
Participant

Signature of the
Head of the Institution

Online Registration Link:
https://forms.gle/VxoPWogbmFDifzMz9

Last date for Registration: 23/12/2022

Note:
- Registration Fee ***NIL***
- Seats are limited and filled in first come first serve basis.
- Attendance is mandatory for all the sessions to get the participation certificate.

Note: All the sessions are conducted offline at department of ECE. The number of participants will be limited to 50.

Chief Patrons:
Dr. Ch. Nageswara Rao, President, Siddhartha Academy of General & Technical Education (SAGTE), Vijayawada

Patrons:
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Sri S. Venkateshwara Rao, Treasurer, SAGTE
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College Advisory Committee:
Dr. A. V. Ratna Prasad, Principal
Dr. B. Panduranga Rao, Prof. of CE & Dean SA
Dr. M.V.S. Raju, Prof. of CE & Dean R&D
Dr. Ch. Srinivas, HoD CE, Dean IR
Dr. N. Vijayasai, HoD ME, Dean Academics

Convener:
Dr. D. Venkata Rao, Professor & Head of ECE

Signal Processing Research Group Coordinator:
Dr. M. Padmaja, Prof. of ECE

Co-ordinators:
Dr. Gunnam Suryanarayana, Associate Professor, ECE
Dr. Venkata Sainath Gupta T, Assistant Professor, ECE
Dr. K. Shri Ramtej, Assistant Professor, ECE

Address for Communication:
Dr. Gunnam Suryanarayana
Associate Professor, ECE Dept., VRSEC
Kanuru, Vijayawada -520007, AP.

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Five Day
Faculty Development Program
On

DEEP LEARNING
IN COMPUTER VISION:
AN END TO END PIPELINE

27th - 31st December 2022

Organized by:
Dept. of Electronics & Communication Engineering

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
(Sponsored by Siddhartha Academy of General & Technical Education)

www.vrsiddhartha.ac.in

About the College:

Velagapudi Ramakrishna Siddhartha Engineering College (VRSEC) was established in 1977 as the first self-financed engineering college in the composite state of Andhra Pradesh. It is located in 24 acres of land at a distance of about 6 Kms from Vijayawada. Currently, the college offers 09 undergraduate programmes and 07 Post graduate programmes including MCA & MBA. VRSEC got recognitions from various accreditation bodies such as National Assessment and Accreditation Council (NAAC) with A+, National Institute Ranking Framework (NIRF) with 141, Atal Ranking of Institutions on Innovation Achievements (ARIIA) – Band A (6- 25 rank), AICTE Margadarshan - Sharing and Mentor Institute, one of the institutions run by Siddhartha Academy of General and Technical Education Recognized as 'Scientific & Industrial Research organization (SIRO)-DSIR, Govt. of India. The institute has 20+ collaborative labs and CoEs in association with SIEMENS, IBM, CISCO, ORACLE, NI, DST, DASSAULT, AP Govt. & so on. Currently, the institute is having 319 dedicated and competent faculty with strong inculcation towards teaching and learning in line with Washington Accord (USA) on OBE and on research.

About ECE Department:

Established in the year 1977, the department of ECE offers B.Tech Programme in Electronics & Communication Engineering with an intake of 240 and M.Tech Programme in VLSI Design & Embedded Systems with an intake of 12.

The department has been accredited by NBA four times. More than 40% faculty are with Ph.D. qualification. The department is led by a team of highly qualified experienced faculty with specializations such as RF & Microwave, Antennas, Digital Signal Processing, Wireless Communications, Digital Image Processing, VLSI and Embedded systems. The department provides excellent academic and research environment to the UG, PG and research students. A Centre of Excellence (TIFAC CORE-DST) in Telematics was established in the year 2009 with the state of the art facilities. Successfully completed many research projects funded by UGC, AICTE, DST, NRSC-ISRO DLRL & ANURAG-DRDO and ongoing projects from AVANTEL and BRNS in Collaboration with BARC, etc. It is also recognized by JNTUK as "Research Center."

About FDP:

Deep Learning is being aggressively invested in by major technology firms since it has become vital in every industry as a means of making machines smarter. The Faculty Development Programme will help to disseminate the knowledge in the domain of Deep Learning (DL). It empowers the participants to understand how Deep Learning can be used to computer vision applications. Upon the completion of FDP, each participant will be able to train a deep learning model and deploy it on NVIDIA GPU board.

Target Audience:

Faculty, Research Scholars and persons from Industry.

Resource Persons:

Dr. Shaik Ahmad
Senior R&D Engineer at KAMERAI, Chennai, Tamilnadu

Dr. K N V P S Rajesh
Assoc. Professor, VIT-AP University.

Dr. T. Sudhakar
Asst. Professor, SRM University, Andhra Pradesh

Dr. Gunnam Suryanarayana
Associate Professor, ECE, VR Siddhartha Engg. College.

Main Contents Covered:

- Challenges and Opportunities in Deep Learning
- Mathematical Foundations of Deep Learning
- Data Priority and Data Augmentation
- Computer Vision Models and their real world applications.
- **Adding Data and Model :** Solving a Vision Problem
- **Case Study:** Development of Deep Learning Models using Transfer Learning