REGISTRATION FORM

FIVE DAY FACULTY DEVELOPMENT PROGRAM
on
"Research Challenges and Opportunities in Signal and Image Processing"
3rd – 7th January 2025

Name: ....

Designation: ....

Institution/Organization: ....

Address: ....

....

....

Contact Number: ....

Email: ....

Qualifications: ....

Experience in years:

Teaching: Research: Industry:

Signature of the Participant	Signature of the Head of the Institution

Registration Link:

https://docs.google.com/forms/d/1oeRuYYXn60f guYz6n6PD6HS6fmenX83MWNv2i8wtACk/edit

Address for Communication:

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Chief Patron:

Sri M. Rajaiah, President, SAGTE & Convener, VRSSOE

Patrons:

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Convener:

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Dr M. Padmaja, Professor, ECE Department, VRSSOE

Organizing Committee:

Dr. A. Vijaya Sankar, Associate Professor

Dr. G. Surya Narayana , Associate Professor

Dr. Fayaz Ahmed, Associate Professor

Mr. G. Hema Kumar, Sr. Assistant Professor

Mr. A. Ravi Raja, Assistant Professor

Mrs. K V Ratna Prabha, Assistant Professor

Dr. T. Pratap, Assistant Professor

Eligibility:

The FDP is open to faculty members, PG students, and Research scholars, persons from industry and R&D organizations across the country.

Target Audience:

Faculty, Research Scholars, PG Students

Registration Fee: ***NIL***

Sponsored by SAGTE

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FACULTY DEVELOPMENT PROGRAM
on**

RESEARCH CHALLENGES & OPPORTUNITIES IN SIGNAL AND IMAGE PROCESSING

3rd – 7th January 2025

Coordinators
Dr. B. Lakshmi Sirisha, Associate Professor
Dr. P. Sarah Suhasini, Associate Professor
Mrs. M. Bindu Priya, Assistant Professor

Organized by:

Dept. of Electronics & Communication Engineering

SIDDHARTHA
ACADEMY OF HIGHER EDUCATION
An Institution DEEMED TO BE UNIVERSITY
(Under Section 3 of UGC Act, 1956)
(Sponsored by Siddhartha Academy of General & Technical Education), Vijayawada, A.P.

www.vrsiddhartha.ac.in

ABOUT THE INSTITUTE:

Velagapudi Ramakrishna Siddhartha Engineering College, (Deemed to be University) (Estd.1977) Vijayawada, Andhra Pradesh sponsored by Siddhartha Academy of General and Technical Education, The 1st private self-financing engineering college in the combined state of Andhra Pradesh. The college has gained autonomous status granted by UGC in 2006, andbecame a university in 2024. All the UG programs & eligible 5 PG programs offered by the college were accredited by NBA in Outcome Based Education under Tier – I format approved by Washington Accord. Only private institute in A.P. selected for MARGADARSHAN scheme by AICTE to share & Mentor institutions in the region for quality up gradation & accreditation of the programs. The College has been ranked among the top 200 engineering institutions by NIRF for the last 5 consecutive years. Only self-financing institution in A.P recognized as a Band-Excellent Institution under ARIIA (Atal Ranking of Institutions on Innovation Achievements) by MHRD. The college is also accredited by NAAC with 'A+' grade, and also certified by "ISO – 21001:2018. Ours is one of the TEQIP S.C.1.2 institutions in the country awarded with a World Bank Grant by MHRD, Govt. of India..

ABOUT THE ECE DEPARTMENT:

We are offering B.Tech (Electronics and Communication Engineering) program with an intake of 240, M.Tech (VLSI Design and Embedded Systems) program with an intake of 12 and also Ph.D. programs in various disciplines. Further, the highlights of the ECE department are as follows: The department of ECE comprises four focused research groups 1. Antennas, 2. Image Processing, 3. RF & Microwave and 4. VLSI & ES. All faculty members actively publish articles in international reputed journals and apply for national project funding agencies. The department has MOUs with IIT Tirupati, NIT Warangal, New Mexico State University, Old Dominion University (Norfolk, Common Wealth of Virginia), Avantel Limited, Talent Sprint Pvt. Ltd. (PEGA), A.P. Skill Development Corporation, etc. 40% of the faculty with Ph.D. qualification from premier institutions encompassing IITs, NITs, BITs, and government universities to institute strong foundation and impart necessary skills. Establishment of TIFAC CORE in Telematics by DST, New Delhi, and industries with an outlay of 10 Crores, the first of its kind in the state of A.P., for producing industry-ready students in the focused core areas.

ABOUT FDP:

The proposed FDP is devoted to the fundamental theory, recent developments and research opportunities addressing the related theoretical and practical aspects, usage of advanced tools and techniques for signal and image processing with machine learning and deep learning. FDP provides an exposure to new areas of research being carried out in the rest of the world.

OBJECTIVES OF FDP:

This Faculty Development Program "Research Challenges and Opportunities in Signal and Image Processing", aims to acquaint faculty and students with recent advancements and research trends in signal and image processing. Discusses the emerging applications and interdisciplinary research opportunities and enhance the understanding of cutting-edge techniques like Machine Learning, Deep Learning and AI.

COURSE CONTENTS:

Research Challenges & opportunities in
- Signal Processing
- Image Processing and Computer Vision
- Machine Learning
- Deep Learning
- VLSI Signal Processing

RESOURCE PERSONS:

Dr. Ch. Viswanadham
General Manager, Bharat Electronics Limited, Hyderabad.

Dr. Rama Krishna Sai Gorthi
Professor & Dean (Academic Affairs) IIT-Tirupati.

Sri. P. Chandrasekhar
Adjunct Professor, IIITDM, Kurnool, Andhra Pradesh, Scientist (Rtd.), NRSC, Hyderabad.

Mr. Sagnik Biswas
Scientist, DLRL, DRDO

Dr. T. Rajasekhar
Senior Physical Design Engineer, Xilinx AMD, Hyderabad.

WhatsApp link:
https://chat.whatsapp.com/DVIWS5fosQv0wrN5SmKDtI

Note: All the sessions are conducted offline at Department of ECE. The number of Participants will be limited to 50. Certificates will be provided to those participants who attend all the sessions of the program and also appear for the test as per the norms.