



# SIDDHARTHA

## ACADEMY OF HIGHER EDUCATION

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An Institution **Deemed to be University**  
(Under Section 3 of UGC Act, 1956)

Kanuru, Vijayawada - 520 007, AP. [www.vrsiddhartha.ac.in](http://www.vrsiddhartha.ac.in)

(Sponsors: Siddhartha Academy of General and Technical Education)

(Formerly **Velagapudi Ramakrishna Siddhartha Engineering College**)

### Notification for Junior Research Fellow (JRF) Position

The Department of **Electronics and Communication Engineering** at **Siddhartha Academy of Higher Education** (An Institution Deemed to be University) (Formerly known as **Velagapudi Ramakrishna Siddhartha Engineering College**) invites applications for the position of **Junior Research Fellow (JRF)** under a research project funded by **ISRO RESPOND**. The details of the project and the JRF position are as follows:

#### Project Details:

Project Title:	Development of Techniques for Scalloping and Banding Removal in Scan-SAR
Sanction Order No. & Date:	No. DS-2B-13012(2)/2/2025-Sec.2, dated 03.02.2025
Implementing Agency:	Department of Electronics and Communication Engineering, Velagapudi Ramakrishna Siddhartha School of Engineering, Siddhartha Academy of Higher Education (Deemed to be University), Kanuru, Vijayawada – 520007, Andhra Pradesh, India
Principal Investigators (PIs):	Dr. Turimerla Pratap; Prof. S. Vasavi; Dr. Bhaskar Dubey (ISRO-SAC)
Project Duration:	Two years

#### Position Details:

- Position:** Junior Research Fellow (JRF)
- Essential Qualifications:**
  - Postgraduate degree in Basic Science or Graduate/Postgraduate degree in a professional course, selected through any of the following:
    - Scholars selected through **National Eligibility Tests** – CSIR-UGC-NET (including lectureship/Assistant Professorship) or GATE.
    - Selection process through national-level examinations conducted by central government departments and their agencies/institutions such as **DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER**, etc.
  - Candidates with a strong academic background and a keen interest in research are encouraged to apply.
- Desirable Skills:**
  - Strong research aptitude.
  - Experience in **image processing, machine learning, or related fields** will be an added advantage.
- Fellowship:** ₹37,000 per month + 20% HRA.
- Additional Benefit:**
  - The selected candidate will have the opportunity to register for a **Ph.D. under Siddhartha Academy of Higher Education** in the Department of ECE.

#### Application Process:

Interested candidates should submit their **updated CV** via email to [tpratap@vrsiddhartha.ac.in](mailto:tpratap@vrsiddhartha.ac.in)

- The selection process will commence upon receipt of applications.
- Shortlisted candidates will be notified via email regarding the interview schedule.
- Last date for receiving the CV via email: **15.04.2025**

**Note:** Only shortlisted candidates will be called for an interview. **No TA/DA will be provided** for attending the interview. For any queries, please contact the Principal Investigator(s) at the above email address.

*D. Venkatesh*  
Professor & Head  
**HOD**  
Dept. of Electronics & Communications  
V.R. Siddhartha Engineering College  
VIJAYAWADA-520 007

*JTG*  
**PRO-VICE-CHANCELLOR**  
Pro Vice Chancellor  
Siddhartha Academy of Higher Education  
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