VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS) - VIJAYAWADA TIME TABLE FOR M.TECH., I-SEMESTER (M.Tech-23) REGULAR EXAMINATIONS - FEBRUARAY, 2024

DATE: 24/01/2024

TIME:10.00AM to 1.00PM

DATE	STRUCTURAL ENGINEERING (CE)	DATA SCIENCE (IT)	VLSI DESIGN & EMBEDDED SYSTEMS (ECE)	AUTOMATED MANUFACTURING SYSTEMS (ME)		
05/02/2024 Monday	23CESE1001 Computer Aided Advanced Structural Analysis	23ITDS1001 Mathematical Foundations for Data Science	23ECVE1001 Design and verification through System Verilog	23MEAM1001 Computer Aided Modelling and Simulation		
07/02/2024 Wednesday	23CESE1002 Soil Structure Interaction	23ITDS1002 Advanced Data Structures and Algorithms	23ECVE1002 ARM Controllers for Embedded Systems	23MEAM1002 CNC & Part Programming		
09/02/2024 Friday	23CESE1003 Retrofitting and Rehabilitation of Structures	23ITDS1003 Machine Learning	23ECVE1003 CMOS VLSI Design	23MEAM1003 Microcontrollers and PLC		
12/02/2024 Monday	23CESE1014 Prog. Elective - I	23ITDS1014 Prog. Elective - I	23ECVE1014 Prog. Elective - I	23MEAM1014 Prog. Elective - I		
14/02/2024 Wednesday	23CESE1015 Prog. Elective - II	23ITDS1015 Prog. Elective - II	23ECVE1015 Prog. Elective - II	23MEAM1015 Prog. Elective - II		
16/02/2024 Friday	23MTMC1026 Research Methodology and IPR					

Elective Course

Elective-I	23CESE1014A Pre-Engineered steel buildings	23ITDS1014A Statistics with R	23ECVE1014A Device Modelling	23MEAM1014A Additive Manufacturing Technologies and Applications
Elective-II	19CESE1015B Prefabricated Structures	23ITDS1015C Cyber Security & Forensics	23ECVE1015A Embedded System Design and Architecture	23MEAM1015B Mechatronics

(Dr. T SA CHOWDARY)
Dy. CONTROLLER OF EXAMINATIONS

(Dr. A V RATNA PRASAD) PRINCIPAL