

V R SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA – 520 007

Date: 17/08/2023

REVISED B.TECH MINORS TIME TABLE FOR B.TECH., VI-SEMESTER (VR20)
EXAMINATIONS, AUGUST-2023 (2020 BATCH)


Time: 2.00 PM to 5.00 PM

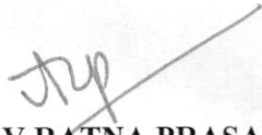
DATE	CE / ECE / EEE / EIE / ME
04/09/2023 Monday	20CSM6701 – Fundamentals of Operating Systems

REVISED B.TECH HONORS TIME TABLE FOR B.TECH., VI-SEMESTER (VR20)
EXAMINATIONS, AUGUST-2023 (2020 BATCH)

Time: 2.00 PM to 5.00 PM

DATE	CSE	IT
04/09/2023 Monday	20CSH6801A – Artificial Neural Networks 20CSH6801F – Backend Technologies	20ITH6801A – Social Media Mining


(Dr. T S R CHOWDARY)
Dy. Controller of Examinations


(Dr. A V RATNA PRASAD)
Principal