

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 26/12//2023

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. I, II SEMESTER SUPPLE (VR20 REGULATION)
EXAMINATIONS HELD IN NOVEMBER -2023**

20BS1101 - Matrices & Differential Calculus

- NIL -

20BS1102B – Applied physics

- NIL -

20ES1103- Programming for Problem Solving

- NIL -

20ES1104 - Basics of Electrical Engineering

- NIL -

20BS2101 - Laplace Transforms & Integral Calculus

- NIL -

20BS2102- Engineering Chemistry

- NIL -

20ES2103B - Python Programming


- NIL -


20ES2104A - Basics of Electronics Engineering

228W1A4251

20ES2104B - Circuit Analysis

- NIL -


CONTROLLER OF EXAMINATIONS
Velagapudi Rama Krishna
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA-520 007.A.P


PRINCIPAL
V.R.Siddhartha Engineering College
AUTONOMOUS
VIJAYAWADA-520 007

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 26/12/2023

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. VI SEMESTER SUPPLE (VR20 REGULATION)
EXAMINATIONS HELD IN NOVEMBER -2023**

20CS6205C – Fundamentals Of Java Programming


- NIL -

20EC6303- Digital System Design Using Programmable Devices

208W1A0424 .

20ME6205A -Mechatronics

- NIL -


CONTROLLER OF EXAMINATIONS
Velagapudi Rama Krishna
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA-520 007.A.P.


PRINCIPAL
V.R.Siddhartha Engineering College
AUTONOMOUS
VIJAYAWADA-520 007