

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 05/09/2024

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. VI SEMESTER SUPPLE (VR20 REGULATION)
EXAMINATIONS HELD IN MAY -2024**

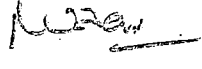
20EE6301 – Power System Analysis
208W1A02A7

20EI6302 - Digital Signal Processing
- NIL -

20EI6404D - Power Plant Instrumentation
208W1A10A0

20ME6205A -Mechatronics
- NIL -


(Dr. T S R CHOWDARY)
Controller of Examinations


(Dr. N VIJAYA SAI)
Dean (Academics & Exams)


(Dr. A V RATNA PRASAD)
Principal