

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 22/04/2024

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. I SEMESTER REGULAR (VR23 REGULATION)
EXAMINATIONS HELD IN JANUARY -2024**

23BS1101 - Linear Algebra & Calculus

- NIL -

23BS1102 - Engineering Physics

- NIL -

23BS1102A – Engineering Chemistry

- NIL -

23BS1102B – Chemistry

238W1A04M8

23ES1103A – Basic Civil & Mechanical Engineering

- NIL -

23ES1103B –Basic Electrical & Electronics Engineering

238W1A0463 238W1A0365

23ES1104 - Introduction To Programming


238W1A04202 238W1A04203 238W1A04221 238W1A04222 238W1A04228 238W1A04230
238W1A04231 238W1A04234 238W1A04235 238W1A04236 238W1A04241 238W1A04242
238W1A04245 238W1A04249 238W1A04251

23HS1105- Communicative English

- NIL -

23ES1105 –Engineering Graphics

238W1A0434



CONTROLLER OF EXAMINATIONS
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