

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 05/09/2024

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. II SEMESTER REGULAR & I SEMETER SUPPLE
(VR23 REGULATION) EXAMINATIONS HELD IN JUNE -2024**

23BS2101 – Differential Equations & Vector Calculus

238W1A0555 238W1A0567 238W1A05H2 238W1A05L4 238W1A05L8 238W1A05N0

23BS2102B - Chemistry

- NIL -

23ES2103A – Basic Civil & Mechanical Engineering

238W1A04I7

23ES2103B – Basic Electrical & Electronics Engineering

- NIL -

23PC2104A – Data Structures

238W1A5450 238W1A54B1

23PC2104C Network Analysis

- NIL -

23PC2104D – Electrical Circuit Analysis

- NIL -

23HS2105 - Communicative English

238W1A0180

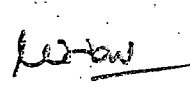
23ES2105 – Engineering Graphics

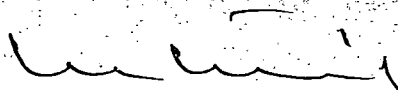
- NIL -

23BS1101 - Linear Algebra & Calculus

- NIL -


(Dr. T S R CHOWDARY)
Controller of Examinations


(Dr. N VIJAYA SAD)
Dean (Academics & Exams)


(Dr. A V RATNA PRASAD)
Principal
Dean (Academics)