

**VELAGAPUDI RAMAKRISHNA  
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7  
(AUTONOMOUS)**

Date: 09/05/2024

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF  
REVALUATION OF B.TECH. I & II SEMESTERS SUPPLE (VR20 REGULATION)  
EXAMINATIONS HELD IN APRIL -2024**

**20BS1101 - Matrices & Differential Calculus**

- NIL -

**20ES1103- Programming for Problem Solving**

208W1A03B1

**20ES2104B - Circuit Analysis**


- NIL -

**20BS2101 – Laplace Transforms And Integral Calculus**


228W1A0445 208W1A1203

**20ES2104C - Network Theory**

- NIL -



**CONTROLLER OF EXAMINATIONS**  
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