

VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS), VIJAYAWADA
TIME TABLE FOR B.TECH., IV SEMESTER SUPPLEMENTARY EXAMINATIONS (VR20 REGULATION), DECEMBER, 2022

DATE: 02/12/2022

VR20

TIME: 2.00 PM to 5.00 PM

DATE	CE	CSE	ECE	EEE	EIE	IT	ME
13/12/2022 Tuesday	20BS4101 Probability & Statistics for Engineers	20BS4101A Probability & Statistics	20BS4101B Signals & Systems	20BS4101C Electrical Measurements & Sensors	20BS4101D Analog Electronic Circuits	20BS4101E Statistics with R	20BS4101F Fluid Mechanics & Hydraulic Machines
15/12/2022 Thursday	20CE4302 Structural Analysis	20CS4302 Advanced Data Structures & Algorithms	20ES4102 Control Systems	20EE4302 Linear Control Systems	20EI4302 Linear Integrated Circuits & Applications	20IT4302 Java Programming	20ES4102A Electrical & Electronics Engineering
17/12/2022 Saturday	20CE4303 Geotechnical Engineering	20CS4303 Computer Organization & Microprocessor	20EC4303 Pulse & Switching Circuits	20EE4303 Electrical Machines-II	20EI4303 Control Systems	20IT4303 Advanced Data Structures & Algorithms	20ME4303 Machine Dynamics
20/12/2022 Tuesday	20CE4304 Hydraulics & Hydraulic Machines	20CS4304 Computer Networks	20EC4304 Analog & Digital Communications	20EE4304 Linear Integrated Circuits & Applications	20EI4304 Industrial Instrumentation	20IT4304 Database Management Systems	20ME4304 Engineering Metallurgy
22/12/2022 Thursday	20HS4105 Universal Human Values						


(Dr. T S R CHOWDARY)

Dy. CONTROLLER OF EXAMINATIONS


(Dr. A V RATNA PRASAD)

PRINCIPAL