V R SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS), VIJAYAWADA

TIME TABLE FOR B.TECH., IV SEMESTER (PHASE-II) REGULAR EXAMINATIONS (VR23), MAY/JULY, 2025

DATE: 24/06/2025

TIME: 2.00 PM to 5.00 PM

DATE	AI&DS	CSE	CSE (AI&ML)	IT
07/07/2025 Monday	23BS4101A Probability and statistics for Data science	23BS4101B Discrete Mathematics and Graph Theory	23HS4101C Optimization Techniques	23BS4101B Discrete Mathematics
09/07/2025 Wednesday	23AI&DS4302 Artificial Intelligence	23BS4102B Probability & Statistics (CSE,AI&ML,IT)		
11/07/2025 Friday	23AI&DS4303 Introduction to Data Science	23CS4303 Operating Systems	23AI&ML4303 Machine Learning	23IT4303 Operating Systems
14/07/2025 Monday	23AI&DS4304 Digital Logic & Computer Design	23CS4304/ 23AI&ML4304 Database Management Systems (CSE, AI&ML)		23IT4304 Database Management Systems
16/07/2025 Wednesday		23CS4305 Software Engineering	23AI&ML4305 Digital Logic & Computer Organization	23IT4305 Software Engineering

(Dr.T S R CHOWDARY)
CONTROLLER OF EXAMINATIONS

(Dr. A V RATNA PRASAD)
PRINCIPAL