

VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS), VIJAYAWADA

VR20

TIME TABLE FOR B.TECH., III SEMESTER SUPPLEMENTARY EXAMINATIONS (VR20 REGULATION), JULY/AUGUST, 2022

DATE: 13/07/2022

TIME: 2.00 PM to 5.00 PM

DATE	CE	CSE	ECE	EEE	EIE	IT	ME
23/07/2022 Saturday	20BS3101 Mechanics of Solids	20BS3101A Discrete Mathematics	20BS3101B Complex Analysis & Numerical Methods (ECE/EIE/IT)	20BS3101C Transformation & Numerical Methods	20BS3101B Complex Analysis & Numerical Methods (ECE/EIE/IT)		20BS3101D Mathematics for Mechanical Engineers
26/07/2022 Tuesday	20ES3102A Engineering Geology	20ES3102B Java Programming	20EC3302 Analog Electronics	20EE3302 Electronic Circuits	20ES3102C Electronic Devices & Circuits	20ES3102D Discrete Mathematical Structures	20ME3302 Mechanics of Materials
28/07/2022 Thursday	20CE3303 Surveying & Geomatics	20CS3303 Operating Systems	20EC3303 Microcontrollers	20EE3303 Electrical Machines-I	20EI3303 Digital Circuits & Systems	20IT3303 Data Structures	20ME3303 Basic Thermodynamics
30/07/2022 Saturday	20CE3304 Fluid Mechanics	20CS3304 Digital Logic & Computer Design	20EC3304 Digital Circuit Design	20ES3104 Network Analysis-II	20EI3304 Sensors & Transducers	20IT3304 Computer Organization	20ME3304 Manufacturing Processes
01/08/2022 Monday	20CE3305 Concrete Technology	20CS3305 Data Structures	20EC3305 Probability Theory & Random Processes	20EE3305 Digital Electronics	20EI3305 Electrical & Electronic Measurements	20IT3305 Operating Systems	20ME3305 Kinematics of Machines
03/08/2022 Wednesday						20IT3308 Object Oriented Programming using C++	

(Dr. T S R CHOWDARY)

Dy. CONTROLLER OF EXAMINATIONS

(Dr. A V RATNA PRASAD)

PRINCIPAL