

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA**


Date: 09/04/2025

NOTICE

In view of the **Internship Program schedule** and the **prevailing heavy summer heat**, the Semester End Examinations for B.Tech IV-Semester (VR23 Regulation) have been **rescheduled** as follows:

Branches	Commencement of Lab Semester End Examinations	Commencement of Theory Semester End Examinations
CE, ECE, EEE, EIE & ME Branches	12/05/2025	10/06/2025
AI&DS, CSE(AI&ML), CSE & IT	07/07/2025 (Commencement of Theory & Lab Examinations)	

All students are **advised to take note of the revised dates** and make necessary preparations.


(Dr. T S R CHOWDARY)
Controller of Examinations


(Dr. A V RATNA PRASAD)
Principal

To
All HODs
Notice Board
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Exam Section