

V R SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS), VIJAYAWADA
TIME TABLE FOR B.TECH., IV SEMESTER (PHASE-I) REGULAR EXAMINATIONS (VR23).
MAY / JUNE, 2025

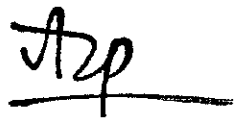
VR23

Date: 29/04/2025

Time: 9.00 AM to 12.00 Noon

DATE	CE	ECE	EEE	EIE	ME
10/06/2025 Tuesday	23HS4101 Engineering Economics and Management (CE,ECE,EEE, EIE&ME)				
12/06/2025 Thursday	23BS4102A Probability and Statistics for Engineers	23ES4102A Random Variables and Stochastic Processes	23ES4102B Electrical Measurements and Sensors	23ES4102C Linear Integrated Circuits and Applications	23ES4102D Engineering Metallurgy
14/06/2025 Saturday	23CE4303 Structural Analysiss	23EC4303 Microcontrollers	23EE4303 Linear Integrated Circuits & Applications	23EI4303 Control Systems	23ME4303 Manufacturing Processes
16/06/2025 Monday	23CE4304 Geotechnical Engineering	23EC4304 Analog Integrated Circuits	23EE4304 Linear Control Systems	23EI4304 Industrial Instrumentation	23ME4304 Fluid Mechanics and Hydraulic Machines
18/06/2025 Wednesday	23CE4305 Hydraulics and Hydraulic Machines	23EC4305 Analog & Digital Communications	23EE4305 Electrical Machines-II	23EI4305 Electrical & Electronic Measurements	23ME4305 Machine Dynamics


(Dr. T S R CHOWDARY)
CONTROLLER OF EXAMINATIONS


(Dr. A V RATNA PRASAD)
PRINCIPAL