



SIDDHARTHA

ACADEMY OF HIGHER EDUCATION

An Institution **DEEMED TO BE UNIVERSITY**

(Under Section 3 of UGC Act, 1956)

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DATE: 13/12/2024

TIME: 10.00AM to 1.00PM

TIME TABLE FOR M.TECH., I-SEMESTER (SU24) REGULAR EXAMINATIONS - DECEMBER, 2024

DATE	STRUCTURAL ENGINEERING (CE)	ARTIFICIAL INTELLIGENCE AND DATA SCIENCE (IT)	VLSI DESIGN & EMBEDDED SYSTEMS (ECE)	CYBER SECURITY(CS)
23/12/2024 Monday	24CESE501 Computer Aided Advanced Structural Analysis	24ITDS501 Mathematical Foundations for Data Science	24ECVE501 CMOS VLSI Design	24CSCS501 Network Security and Forensics
26/12/2024 Thursday	24CESE502 Theory of Elasticity and Plasticity	24ITDS502 Advanced Data Structures and Algorithms	24ECVE502 Design And Verification Through System Verilog	24CSCS502 Application Security, Vulnerability Assessment and Penetration Testing
28/12/2024 Saturday	24CESE503 Structural Dynamics	24ITDS503 Machine Learning	24ECVE503 ARM Controllers For Embedded Systems	24CSCS503 Incident Response and Threat Intelligence
30/12/2024 Monday	24CESE504 Prog. Elective - I	24ITDS504 Prog. Elective - I	24ECVE504 Prog. Elective - I	24CSCS504 Prog. Elective - I
02/01/2025 Thursday	24CESE505 Prog. Elective - II	24ITDS505 Prog. Elective - II	24ECVE505 Prog. Elective - II	24CSCS505 Prog. Elective - II

Elective Course

Elective-I	24CESE504A Advanced Steel Buildings	24ITDS504A Data Visualization And Interpretation	24ECVE504A Device Modelling	24CSCS504A Introduction to Forensics Science and Cyber Law
Elective-II	24CESE505B Precast And Prefabricated Structures	24ITDS505A Artificial Intelligence Essentials and Applications	24ECVE505A Embedded Systems Design and Architecture	24CSCS505C Secure Software Engineering


(Dr. T ANURADHA)

CONTROLLER OF EXAMINATIONS