

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHAENGINEERINGCOLLEGE :: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 08/10/2024

**LIST OF REGD. NO.OF SUCCESSFUL / BENEFITTED CANDIDATE BY WAY OF
REVALUATION OF B.TECH. IV & VI -SEMESTERS HONORS MINORS (VR20
REGULATION) EXAMINATIONS HELD IN AUGUST - 2024**

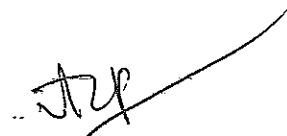
20CSH4801E – Front End Technologies

- NIL -

20CSM6701 – Fundamentals of Operating Systems

- NIL -


**CO-ORDINATOR OF EXAMINATIONS
Velagapudi Rama Krishna
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