

**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE, KANURU,
VIJAYAWADA, ANDHRA PRADESH-520007**

SRG/2019/002236

14.12.2019

ADVERTISEMENT FOR THE POST OF JUNIOR RESEARCH FELLOW IN ECE DEPT

Applications are invited from eligible candidates for the temporary position of **Junior Research Fellow (JRF)** post for **SERB** sponsored research project titled “**Optimization of low cost lab-on-chip platforms for the development of biomedical applications**” in the Department of Electronics & Communication Engineering, Velagapudi Ramakrishna Siddhartha Engineering College, Kanuru, Vijayawada.

Position	: Junior Research Fellow (JRF)
Number of post	: One
Essential qualification	: B.E/B.Tech in Electronics & Communication Engineering with minimum of 70% marks or 7.5 CGPA and M.E/M.Tech or Equivalent in Microelectronics and VLSI/Biomedical Engineering with 70% marks or 7.5 CGPA from any recognized University/Institute.
Salary	: Rs. 31,000 per Month (consolidated) for two years
Duration of Project	: 2 years
Job Description	: 1. Purchase of equipment and components for the project 2. Simulation using Microfluidics Module for use with COMSOL Multiphysics simulators 3. Documentation of the results.

Additional Information:

1. Preference will be given to the candidate who is having good academic record, research potential and relevant experience.
Eligible candidates may send their applications on the prescribed format along with Bio-Data and supporting documents by post to **Dr. Matta Durga Prakash, Associate Professor, Department of Electronics and Communication Engineering, Velagapudi Ramakrishna Siddhartha Engineering College, Kanuru, Vijayawada, Andhra pradesh-520007**, with a scan copy email to: **mdprakash82@gmail.com** with the subject marked as “**APPLICATION FOR THE POST OF JRF SERB ECE DEPT**”. **The last date for receipt of applications (hardcopy) is 27.12.2019 by 5.00 P.M.** The date of interview will be communicated for the shortlisted candidates by E-mail.
2. No T.A/D.A will be paid for attending the interview.
3. Interview will be held at V. R. Siddhartha Engineering College, Kanuru, Vijayawada, Andhra pradesh-520007.
4. Selected candidates are expected to join immediately.

**Dr. Matta Durga Prakash
Principal Investigator**

OFFICE OF THE DEAN (R & D), V.R. SIDDHARTHA ENGINEERING COLLEGE, KANURU,
VIJAYAWADA, ANDHRA PRADESH-520007
**RECRUITMENT OF TEMPORARY PROJECT STAFF - APPLICATION FOR THE POST
OF JUNIOR RESEARCH FELLOW (JRF)**

<p>For Office Use only</p> <p>Serial Number allotted to the Applicant</p> <p>Signature of the Application Screening Committee Chairman</p>	<p>Paste here your recent passport size colour Photograph</p>
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APPLICATION FOR THE POST OF JRF

1. Name of the Candidate in full	
2. Nationality	
3. Mother Tongue	
4. Date of Birth	Date Month Year
5. Sex	Male / Female
6. Marital Status	Married / Single
7. Category SC / ST / OBC / General	Please specify
8. Address for communication with Mobile number and E Mail ID	
9. Project Title	
10. Name of the Post applied for	
11. Advertisement Number with Date	

Academic Career: Enclose photo copies of the qualifying degree certificates and mark sheets/ grade cards showing the percentage of marks or CGPA from HSC onwards

Name of Exam	School/University	Board/University	Broad subjects of study	Year of Passing	% of marks/ CGPA
S.S.L.C					
H.S.C					
Graduation					
Post-Graduation					
Any other					

(Enclose self-attested photocopies)

12. GATE Score (out of 100).....SubjectYear.....Qualified: Yes/No
(Enclose self-attested photocopies)

13. Work Experience, if any

Organization	Designation	Duration		Nature of Job
		From	To	

14. Previous Research work and publication, if any (*enclose copies of published or unpublished work*)

15. Reference

Name	Address	Phone & E Mail ID

16. **Declaration:** I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date:

Full signature of the Applicant

List of Enclosures: (1)
 (2)
 (3)
 (4)