FEEDBACK and ACTION TAKEN REPORTS AY 2017-18

V R SIDDHARTHAENGINEERING COLLEGE::VIJAYAWADA DEPARTMENT OF INFORMATION TECHNOLOGY

Academic Year	Feedback collected	Action taken
	To introduce the course "Theory of Automata and Formal Languages" for GATE	Theory of Automata and Formal Languages is introduced as an elective course
	To introduce the courses "Mobile Computing" and "Natural Language processing " to meet the current trend	"Mobile Computing" and "Natural Language processing " were introduced in VR17 Regulations to meet the current trends and technologies.
	To increase the courses under Programming domain.	Course like Dot Net programming, Python Programming, Advanced Programming were introduced as a part of curriculum of VR17 Regulations.
		Program Specific Outcomes are defined for the IT program VR17 curriculum is designed in line with AICTE Regulations
2017-2018	To include the courses that help for improving the placements of the students	R Programming, Advanced Programming, Python Programming, Blockchain technologies and Cloud based CRM Platform (Salesforce) are introduced under VR17 Regulations
		Training in terms of reasoning, aptitude, technical skills was provided for the students and mock interview are conducted for preparing the students for placements
	To conduct the classes for improving communication skills	Soft skills courses are introduced in VR17 Regulations.
	To organize the workshops that improve the technical skills of the students	Workshops and seminars are organized for the students on latest technologies.
		First year syllabus of VR17 curriculum is designed with new courses like Technology and Society, Computer Problem Solving
	To organize industrial tours for students periodically	University Innovation Fellowship organized in collaboration with Stanford University,

	Microsoft, Google and APSSDC(Andhra Pradesh State Skill Development Corporation) and visited USA
To conduct more workshops on skill development and provide practice in group discussions	Students are trained under QEEE for enhancing the technical skills Microsoft WISE(Women In Software Engineering) program is introduced for girl students to shape out successful career in technology
To improve Technical skills in addition to theoretical concepts as companies are looking for more practical knowledge	Training classes were conducted on Mobile Application Development by APSSDC Students are trained under IBM career education program
To enhance the programming skills of the students	Coding lab is enhanced considering tasks from hackathon and hacker rank
To increase student organizations to sustain student professional activities	ACM and CSI student chapters are activated and conducting events under these professional activities.
To motivate the students to be part of social service organizations	Students are encouraged to enrol under NSS
To introduce Selenium with Java and also focus on Manual Testing and Functional Testing	Lab tasks of Software Testing Tools lab focus on Selenium Testing Tool.
To include Angular JS in Web Development	Angular JS and Node JS are included in Web Programming
To give the tasks from hackathon as home assignments instead of theoretical concepts.	Coding lab is enhanced considering tasks from hackathon and hacker rank.
To focus on testing as it is very much useful at initial days of job at industry	Software Testing Lab course is introduced in 7 th semester of VR17 Regulations.
To offer GATE coaching for the students	Few courses like Computer Organizatin and Theory of Automata and Formal Languages are introduced in VR17 Regulations.
To provide the facilities for self-learning courses.	Courses under MOOCs, are introduced and are made mandatory for the students in V and VIII semesters of VR17 Regulations.
	Online Certification courses are made mandatory for the students and faculty
To include value added courses in Gaming, Design Thinking, Film Making and Photography.	Value added courses like R Programming, IoT, NS2, Matlab, Knime Tool and Selenium Testing

	are offered to the students.
To introduce the course "Service Oriented Architecture" to improve the student's knowledge at architecture level.	SOA is introduced during VII semester of VR17 Regulations.
To introduce Operating System Laboratory in third semester and teaching in lab is required for first half an hour	Operating System course is implemented as learning by doing course by offering lab hours.
To replace "Probability and Statistics" with "Statistics with R"	Probability and Statistics" course in VR14 Regulations has been replaced with "Statistics with R" in VR17 Regulations.
To introduce "Object Oriented Programming" theory course in place of "Java Programming"	Object Oriented Programming is introduced in third semester under VR17 Regulations.
To implement Data Structures lab in C++	Data Structure is implemented both in C and C++
To improve the activities in coding lab	Coding lab is enhanced considering tasks from hackathon and hacker rank
To introduce Value Added courses from third semester onwards and let the students develop applications and host them to the web site and deal with the back end connections.	Value added courses like R Programming, IoT, NS2, Matlab, Knime Tool and Selenium Testing are offered to the students
To motivate the students for internships	Students under CBCS undergone internships for implementing project work
To float more number of electives	More number of electives are offered for the students
To increase programming related courses	Python Programming, Object Oriented Programming, R Programming, Dot Net Prgoramming etc., are introduced
To introduce Foreign languages in VR17 curriculum	Foreign languages are introduced under Humanities Elecitve course in Second year under VR17 Regulations.

DEPARTMENT OF <u>ELECTRONICS & COMMUNICATION ENGINEERING</u> VR SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
2017-2018	Freedom in the choice of courses More elective courses required Introduce industry oriented elective courses	Many open electives, program electives and industry needed courses are introduced in the new curriculum (VR17) at different semesters. More no. of students are encouraged to enrol in certification courses offered by NPTEL under MOOCS
	More weightage must be given to projects	Project work is stretched over three semesters with a prototype as the end Product.(around 14 credits)
	Hands on training for different simulation software must be included	In house training is offered in all the tools required
	Digital submission of assignments with anti-plagiarism software	MOODLE is used for digital submission Anti plagiarism software is used to check the validity of the project reports submitted by the students

DEPARTMENT OF <u>COMPUTER SCIENCE & ENGINEERING</u> V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
	Motivate the students to enrol in certification courses.	Encouraged more no. of students to enrol in certification courses offered by NPTEL under MOOCS
		The students are motivated to do certification courses offered by international certification agencies like ORACLE, CISCO.
2017-2018	Facilitate the students with internships at reputed industries.	Identified and signed MOUs with industries for internships. And large no. of students utilized this facility to undergo internship.
	Include the courses, which can improve the problem solving skills.	The Problem Solving methods course was introduced in the VR17 curriculum for First year students. Computational problem solving concepts and MAT Lab fundamentals were introduced in the course.

DEPARTMENT OF <u>ELECTRICAL & ELECTRONICS ENGINEERING</u> V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
	Second year students suggested to include courses IOT, micro controllers, Programming courses like Aurdino, Roseberry pi, PLCs	IOT and Roseberry pi will be offered as open elective -II (inter-disciplinary courses) by other departments. Introduced 8051 and Aurdino in Microcontrollers (17EE3503) of V semester. Introduced PLCs as Programme Elective-1 in sixth semester
	Third year students recommended for 8051 microcontrollers and more Elective subjects	Introduced 8051 in Microcontrollers (17EE3503) of V semester and Introduced five Open electives, Humanities Electives, Six Programme Electives.
	Final year students recommended reducing the content in Power Electronics.	Power Electronics (17EE3602) syllabus is modified.
	To introduce Renewable energy systems course.	Introduced as Open elective course-II (Inter disciplinary Elective-17EE2505B) in V semester.
2017-2018	To delete AC potentiometers.	AC Potentiometers is deleted from Electrical Measurements (17EE3402) course.
	Recommended for more contact hours for Digital Electronics (3L to 4L).	In VR17 regulation contact period is 60 minutes (3L). In VR14 regulation contact period is 50 minutes.
	To include Yoga and sports in curriculum.	Under Humanities Elective of IV semester introduced Yoga
	More Practical sessions	Included two lab sessions in every semester Siemens offer courses under APSSC in VRSEC. Introduced Engineering projects in community services in sixth semester
	Recommended to put more efforts in improving the knowledge base exposure.	Conducting workshops right from First semester onwards.
	More T & P classes	Maximum 4 soft skill courses are introduced in curriculum and spread over four semesters (I, II, III& IV).

DEPARTMENT OF <u>ELECTRONICS AND INSTRUMENTATION ENGINEERING</u>

V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
	Introduce career guidance classes	Career guidance is introduced from III semester onwards
2017-2018	Introduction of subjects, Digital system design using Verilog, Advanced sensors, Programmable Automation Controller Systems (PACS) to make the students industry ready.	Industrial Internet of Things, Drives and Control for Industrial Automation, Fundamentals of Petrochemical Engineering, Sensor Signal Conditioning subjects are included in VII semester of VR17 curriculum
	Improve industry institute interaction	Interaction programs with industry persons is arranged
	Encourage inter discipline projects	Students are encouraged to do inter discipline projects
	Conduct special classes for GATE examination	Conducted domain knowledge classes

DEPARTMENT OF <u>CIVIL ENGINEERING</u>

V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
	Suggested program specific outcomes	Introduced two program specific outcomes in VR17 curriculum.
	Conduct GATE classes	GATE classes were conducted by in-house faculty as per their specialization wise
	Syllabus to match with competitive exams in VR17 curriculum	VR17 Curriculum is being designed to match with competitive exams
2017-2018	Increase industry interaction program	Guest Lectures, seminars / workshops with industry persons are arranged. Alumni entrepreneurs' day was conducted to improve interaction between academia and industry.
	Improve soft skill training program	Subjects like 1) Communication skills laboratory 2) Personality Development and campus recruitment training 3) Quantitative Aptitude are introduced in VR17 Curriculum

DEPARTMENT OF MECHANICAL ENGINEERING VR SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
	Suggested 2 program specific outcomes	Two Program Specific Outcomes are introduced and approved by DAB and BOS
	To introduce a course on Foreign language in VR17 curriculum	A course on Foreign language was introduced in the curriculum for 2 nd year students in IV semester (VR17 regulations) 17HS2406I Foreign Languages (German/French)
2017-	To increase inter disciplinary knowledge through new and modified courses	Inter disciplinary courses are included in the curriculum (VR17 regulations) under open electives in V and VI semesters.
2018	To introduce career guidance classes	Career guidance classes are introduced from III semester onwards.
	To improve the campus placements for students in core industries	Special Placement training in core engineering given to students before the campus placement.
	To increase industrial interaction	Interaction programs with industry persons are arranged
		Students are encouraged to do Internships in industries

DEPARTMENT OF <u>BUSINESS MANAGEMENT</u> V R SIDDHARTHA ENGINEERING COLLEGE::VIJAYAWADA

Academic Year	Feedback collected	Action taken
	To consider the changes in course structure of MBA keeping in view the industry requirement.	Change in course structure was reflected in the MBA-19 regulation as per industry requirement.
	To encourage faculty to acquire technical knowledge especially the courses like SPSS.	Faculty have participated and completed SPSS short-term course.
	To insist on the faculty to publish research papers in Scopus / SCI journals.	Faculty members are asked to publish research papers in Scopus / SCI Journals and are in the process of publication.
2017-2018	To organize more industrial visits to gain practical knowledge.	Industrial study visits organized to the students.
2017 2010	To facilitate more ICT enabled teaching.	More ICT enabled teaching has been initiated.
	To conduct remedial classes and bridge courses for non commerce and management students.	Remedial classes and Bridge courses are being held.
	To conduct special skill development programs.	Skill development and Training programs were conducted to enhance the employability of student.
	To conduct more group discussion sessions and paper presentations in the class room.	More group discussion and paper presentation sessions were held classroom.

DEPARTMENT OF MASTER OF <u>COMPUTER APPLICATIONS</u> V R SIDDHARTHA ENGINEERING COLLEGE

Academic Year	Feedback collected	Action taken
	Make students employable by providing hands on training.	Introduced hands on training in workshops.
2017-2018	At least a minimum number of paper presentations and participating technical events should be mandated for each student.	Students have participated quiz, paper presentation events at various technical competitions.
	Conduct bridge courses as and when necessary.	Conducted bridge course for lateral entry students on the course Computer Networks and Operating Systems.