FEEDBACK and ACTION TAKEN REPORTS AY 2018-19

V R SIDDHARTHAENGINEERING COLLEGE::VIJAYAWADA DEPARTMENT OF INFORMATION TECHNOLOGY

Academic Year	Feedback Collected	Action taken
	To introduce more courses related for placements	Firewalls, IPV4 and IPV6 are included in the course entitled "Computer Networks" The course content of Data Mining has been modified with the inclusion of "Outlier Analysis" IOT syllabus is modified with the inclusion of Security topics.
	To refine the Java Syllabus including java assertions	Java syllabus has been modified in VR17 Regulations
		Angular JS and Node JS are included in Web Programming
2018-2019	To introduce more courses related to programming	Advanced Programming is offered as lab course Dot Net Programming, Python Programming, R Programming are introduced in VR17 Regulations
	To introduce courses for better placements	Python programming syllabus has been modified by including the topics related to database connectivity Advanced Programming is offered as lab course
	To have discipline among the students	Vigilance duties are implemented to in the morning and afternoon sessions to monitor the students who are coming late for the classes and to maintain discipline among the students
	To introduce more workshops to enhance the technical skills of the students	RPA is offered as a value added course Workshops and seminars are organized on programming and current technologies
	To introduce more courses on latest technologies like sales force	Cloud based CRM Platform (Salesforce), Devopps essentials, Blockchain Technologies were introduced as an elective course in seventh semester of VR17 Regulations.
	To take steps in improving the communication skills of the students	Soft skills courses are introduced from 3 rd semester to 6 th semester to improve the logical, quantitative aptitude and communication skills

	of the students.
To introduce more value added courses	RPA and AWS are offered as a value added courses
To introduce case studies in the last unit of the syllabus	Case studies are included for most of the courses
To offer a course related System Administration and Resource Management	The course "Service Oriented Architecture" has been introduced for final year students of VR17 Regulations.
To introduce Power BI to the students for Data Visualization	Tableau is used for implementing Data Visualization tasks
Suggested to see that more number of students undergo global certification courses	Online Certification courses are made mandatory for the students and faculty
To design lab tasks in complex manner and should be more generic rather than specific.	Lab experiments were implemented with coding rather than using tools
Instructed the students to register for code tantra to practice Hadoop and Big data concepts.	Coding labs are introduced from second year to final year to enhance the technical skills of the students.
To include case studies and Project based tasks in all the courses	Case studies are introduced for all the lab courses

DEPARTMENT OF <u>ELECTRONICS & COMMUNICATION ENGINEERING</u> VR SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback Collected	Action taken
	Motivate the students to enroll in certification courses.	Motivated and enrolled the students in certification like CLAD(Certified LabVIEW Associate Developer)
		30 students were trained and CLAD certified
2018-2019	Facilitate the students with internships at reputed industries	Internship is made mandatory in the curriculum for both UG/PG students Several PG/UG students are pursuing their academic projects in reputed Industries Industry based projects are being carried out in BSNL, Doordarshan, DLRL, DRDO, Efftronics systems ltd etc
	Hands on training for different simulation software must be included	Training is offered for students in MATLAB, C Programming and Labview tools
	Include the courses, which can improve the problem solving skills.	T& P cell is conducting coding practice and skill development classes as part of curriculum

DEPARTMENT OF <u>COMPUTER SCIENCE & ENGINEERING</u> V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback Collected	Action taken
	Motivate the students to participate in Hackathon programs and Certification Courses.	All the students of II/IV & III/IV B.Tech are encouraged to solve the Hackathon problems in Competitive coding lab, so that they become ready to participate in more no. of Hackathons. Motivated and enrolled the students in certification courses offered by international certification agencies like ORACLE, CISCO.
2018-2019	Conduct Coding classes for improving programming skills	Conducted competitive coding classes for both II/IV & III/IV B.Tech students to improve programming skills One Competitive coding course has been introduced in VR17 Curriculum to improve the programming skills.
	Introduce more industry need courses, which make students ready to enter in the industry	New courses on latest trends and technologies in industry have been introduced in the VR17 Curriculum.

DEPARTMENT OF <u>ELECTRICAL & ELECTRONICS ENGINEERING</u> V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback Collected	Action taken
	Final year students suggested adding 8086 course, Basics of Siemens courses, PLCs Aurdino, JAVA, Matlab course & IOT, New software technologies. Advanced micro controller or technology, C language in semester-I and advanced in C in Semester-II.	Even after incorporating 8086 topic based on the student feedback the BOS members are not suggesting it as its usage is not there in the present industry and processors are been replaced by microcontrollers. New software courses and tools were incorporated in the new VR17 regulation under open elective and regular courses.
	To execute mini project or major project in Siemens To remove Basics of computer,	Students were encouraged to do projects at Siemens Laboratory. Since management skills are needed it is not
	Engineering economics course.	removed from the curriculum.
	More basic electronics courses.	Workshops are being conducted from first year to get better hands on experience in basic electronics.
2018-2019	Add co-curricular activities and clubs to improve skills.	Students were allowed to participate in the events conducted by the department and prizes were also given to encourage the students.
	Suggested to include concepts related to Electrical safety of human to be explored	It is being taught when students are working in laboratories through charts.
	Recommended for more practical sessions rather than theory, advanced courses on new technology and as per gate syllabus.	To get practical exposure and to compete with industrial requirements in advanced technologies modifications are made in program electives, out of four courses one course is related to advanced laboratory courses like digital controllers Lab, PLC & SCADA Lab and Industrial Drives Lab are implemented in Seventh & Eight Semesters of VR17 Regulation.
	Suggested for more core courses like BEE and delete Basics in civil, Mechanics and chemistry Course and reduce content in mechanics. Delete drawing and include advanced CAD course in first year.	Suggestions will be considered for the next regulation.

Suggested for deep concepts in Microcontrollers, Electronics.	Faculty are encouraging the students to carry more hardware projects linked Microcontrollers applicable for societal problems.
Suggested to reduce Syllabus of Electrical measurements, Electrical Machines, EMFT, Power system Protection, PSA and PE	Modified and revised the syllabus for few of the courses like Power system Protection, PSA and PE.
More hardware projects are required.	It is mandatory that the Mini-projects are hardware projects and encouraging the students to take up the main projects also mostly hardware.
Suggested for more objective type of questioning and modify syllabus for the course switch gear.	One of the internal assessments is carried through online objective type and revised switch gear syllabus.
Modernize PE lab	In PE lab usage of microcontrollers and DS is encouraged.
Recommended for Department library and better placement activity, mock interviews, more Elective courses.	Conducting Technical classes, aptitude classes and mock interviews for better placement. VR17 Curriculum is designed with six program electives and five open electives.
Include mini projects from second year onwards.	In VR17 Regulation, EPIC is implemented.
Add AI to Electrical Systems course. To delete topics like solution to difference equations, DFS in Digital signal Processing course (17EE3404). To delete state diagrams Moore & Mealey models, Binary addition and subtraction and reduce syllabus, Synchronous counter design using state diagrams also shift logic families in unit-I to Unit-IV of Digital Electronics (17EE3305).	Implemented the suggestion. These Suggestions for modification of syllabus in Digital signal Processing and Digital Electronics will be taken into consideration for the next curriculum revision
To delete Thermal modelling, various duty cycles of unit-I, transformers fed controlled converters in Unit-II, Speed control method ward Leonard in Unit-III of Industrial course.	Suggestions given for the course Industrial drives are implemented VR17 regulation. Modified and revised the syllabus in Industrial Drives of VR17 regulation.
Core Knowledge requirement from Industries and More T & P classes.	Training programmes are being conducted to the students in aptitude reasoning etc. In addition to the regular curriculum.
To concentrate on non-core sector	Domain knowledge classes were conducted in addition to the placement training programs at central level.

,	To include more text books for student	New books are being included every year as
	availability.	per the requirement of the students given by
		the faculty in addition to the e books and
		journal available in e-library of the college.

DEPARTMENT OF <u>ELECTRONICS AND INSTRUMENTATION ENGINEERING</u>

V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback Collected	Action Taken
	To offer more training and placement classes	Training classes are conducted from II year onwards targeting placements in
	Classes	software and core industry
	Hands-on Learning through Internships	Internship is made mandatory for all students in VR17 Curriculum
	Introduce ARM controller and	Introduced the suggested subjects in
	Instrumentation with Python to expose	VR17 curriculum in V and VIII
2018-2019	the student to the breadth of the discipline	semesters respectively
	To introduce more value-added courses	Technical English and communication skills, Spoken English, PLC and SCADA by Seimens Centre of Excellence, LabVIEW Core-I, LabVIEW Core-II, Data acquisition and signal conditioning using NI my DAQ, NI Real time and FPGA implementation using NI my RIO by NI Academy are introduced from II semester onwards
	To introduce more workshops to enhance the technical skills of the students	Conducted workshops in new technologies and domains
	To include the courses that help for the placements of the students	Included multiple core courses through program electives from VI to VIII semester

DEPARTMENT OF <u>CIVIL ENGINEERING</u> V R SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback Collected	Action taken	
	Provide full time industry training programme	Field trips were arranged for all students for getting practical knowledge.	
	Increase current software skill programs	Software training Programs were increased	
	Improve software training program		
2018-2019	To provide Full time internship for students in 8 th semester	Students who completed their subjects in CBCS have attended full time internship about 3 months in 8 th semester. Core placements improved	
	Recommended to conduct theory and labs separately	As per the revised curriculum guidelines of AICTE some courses are designed as theory and practice courses	

DEPARTMENT OF MECHANICAL ENGINEERING VR SIDDHARTHAENGINEERING COLLEGE

Academic Year	Feedback Collected	Action taken
	To include self-learning courses for III year	The following self-learning courses are included in B.Tech 3rd year curriculum (VR17 regulations):
		Unconventional Machining Processes Work Study
		The students can complete these courses in MOOCS platform such as NPTEL, etc
	To Increase inter disciplinary knowledge through new and modified courses	Inter disciplinary courses are included in the curriculum (VR17 regulations) under open electives in V and VI semesters. They are:
2018-19		 Traffic Safety Building Services Engineering Fundamentals of Java Programming Artificial Neural Networks Industrial Electrical Systems Cyber Security Data Visualization Air and Noise Pollution Environmental Impact Studies Green Building and Sustainability, etc
	To improve the campus placements for students in core industries	Campus recruitment training programs in core engineering as well as soft skills are provided to the students.

DEPARTMENT OF <u>BUSINESS MANAGEMENT</u> VR SIDDHARTHA ENGINEERING COLLEGE::VIJAYAWADA

Academic Year	Feedback Collected	Action taken
	To obtain industry collaboration and membership in professional bodies.	Obtained life membership of Association of Indian Management schools (AIMS) for MBA department
	To constitute Management Development Clubs.	Management development club and Hindu-Business line club were constituted
	To include new industry – relevant electives in the MBA19 regulations with dual specialization.	Included few industry relevant subjects as electives.
2018-2019	To introduce new subjects and revision of syllabus as per BOS recommendations.	Syllabus revised and new subjects introduced.
	To guide students about enrolling for online certificate courses.	Guided students for enrolment of online courses
	To provide more employability skill training to the students.	Extensive employability skill training is provided to students.
	To provide short term internships to the students.	Summer internship (Major project) is provided.
	To facilitate more case discussion in the class rooms.	Special focus on case discussions in the classroom.

DEPARTMENT OF MASTER OF <u>COMPUTER APPLICATIONS</u> V R SIDDHARTHA ENGINEERING COLLEGE

Academic Year	Feedback Collected	Action taken
	Provide the facilities for self- learning courses. Include value added courses in the	Extended Wi-FI and computing facilities to enable the students to enrol for self learning courses online. Provided the study material and links to students for MOOCS. Conducted training programmes on
	concepts like Gaming and Puzzle solving.	Gaming.
2018-2019	Facilitate the students with more number of books in the department library for learning. At least a minimum number of	Facilitated more number of volumes in the department library and provided elibrary access (IEEE) for the students. Motivated the students for enrolling and
	certification courses should be mandated for each student. Introduce "Statistics with R".	completing a minimum number of certification courses. Introduced "Statistics with R" in the second semester.