

VELAGAPUDI RAMAKRISHNA  
SIDDHARTHA ENGINEERING COLLEGE :: VIJAYAWADA - 7  
(AUTONOMOUS)

Date: 17/12/2020

LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF  
REVALUATION OF B.TECH. III-SEMESTER SUPPLE (VR17 REGULATION)  
EXAMINATIONS HELD IN OCTOBER - 2020

17MA1301A - Complex Analysis and Numerical Methods(CE)

- NIL -

17CE3302 - Introduction to Mechanics of Solids

- NIL -

17CE3303 - Fluid Mechanics

- NIL -

17CE3304 - Surveying & Geometrics

- NIL -

17CE3305 - Engineering Geology

- NIL -

17EC3302- Analog Electronics

- NIL -

17EC3304- Digital Circuit Design

- NIL -

17EC3308 - Signals & Systems

- NIL -

17EE3302 - Electronic Circuits

- NIL -

17EE3304 - Network Analysis-II

- NIL -

17EE3305 - Digital Electronics

- NIL -

17MA1301A - Complex Analysis and Numerical Methods(EIE)

178W1A1083 188W1A1032

17EI3302 - Network Theory

- NIL -

17EI3303 - Analog Electronic Circuits

- NIL -

17MA1301A - Complex Analysis and Numerical Methods(IT)

178W1A1290

17IT3308 - Object Oriented Programming

178W1A1270

17MA1301D - Mathematics for Mechanical Engineers(ME)

- NIL -

17ME3302 - Mechanics of Materials

188W1A03D6

17ME3303 - Basic Thermodynamics

- NIL -

17ME3305 - Kinematics of Machines

- NIL -



CONTROLLER OF EXAMINATIONS  
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