

VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE :: VIJAYAWADA - 7
(AUTONOMOUS)

Date: 05/12/2020

LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. VI SEMESTER SUPPLE (VR14 REGULATION)
EXAMINATIONS HELD IN OCTOBER 2020

14CE3603- Design of Concrete Structures-II

158W1A01D3 168W1A0134

14CE3605- Environmental Engineering -II

168W1A0120

14CS3604 - Theory of Computation

- NIL -

14EC3601 - Linear Integrated Circuits and Applications

- NIL -

14EC3603 - Antennas and Wave Propagation

168W1A04G0

14EC3604 - VLSI Design

168W1A04O0

14EC3605 - Digital Signal Processing

158W1A04N2 168W1A0411 168W1A0494 168W1A0496 168W1A04F6 178W5A0441

14EE3601- Fundamentals of Digital Signal Processing

- NIL-

14EE3602- Power Electronics

- NIL-

14EE3605- Power Systems -II

- NIL -

14EI3601- Virtual Instrumentation

- NIL -

14EI3602 - Industrial Electronics

- NIL -

14EI3603- Industrial Communications Networks

- NIL -

14EI3605- Process Control

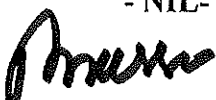
178W5A1019


14ME3601 - Heat Transfer

168W1A0367 168W1A03A5

14ME3602 - Design of Transmission Elements

- NIL-


CONTROLLER OF EXAMINATIONS
Velagapudi Rama Krishna
SIDDHARTHA ENGINEERING COLLEGE
(AUTONOMOUS)
VIJAYAWADA-520 007.A.P.


PRINCIPAL
V.R.Siddhartha Engineering College
AUTONOMOUS
VIJAYAWADA-520 007