

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE :: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 05/12/2020

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. VII-SEMESTER SUPPLE(VR14 REGULATION)
EXAMINATIONS HELD IN OCTOBER - 2020**

14CE3702 – Foundation Engineering

- NIL -

14CE3704 – Design of Steel Structures

158W1A01F5 168W1A0163

14CS3701 – Compiler Design

- NIL -

14CS4705C – Artificial Intelligence

- NIL -

14CS4705D – Software Project Management

- NIL -

14EC3703 – DSP Processors and Architectures

- NIL -

14HS1704 – Engineering Economics And Finance (ECE)

- NIL -

14EE3702 – Switch Gear & Protection

- NIL -

14EE3703 – Power System Analysis

- NIL -

14EE4706/1 – Electrical Distribution Systems

168W1A02B1

14EI3702 – Computer Control of Processes

168W1A1061 178W5A1007

14EI4705/3 – Process Modelling and Simulation

158W1A1002 168W1A1096

14EI4705/4 – Digital Image Processing


- NIL -


14ME3701 – Finite Element Method

- NIL -

14ME3703 – Refrigeration and Air Conditioning

158W1A0328 158W1A03D8 168W1A0307 168W1A0308 168W1A0317


CONTROLLER OF EXAMINATIONS
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