

**VELAGAPUDI RAMAKRISHNA
SIDDHARTHA ENGINEERING COLLEGE:: VIJAYAWADA - 7
(AUTONOMOUS)**

Date: 06/02/2021

**LIST OF REGD. NO. OF SUCCESSFUL / BENEFITTED CANDIDATES BY WAY OF
REVALUATION OF B.TECH. II SEMESTER REGULAR & I&II SUPPLE
(VR17 REGULATION) EXAMINATIONS HELD IN NOVEMBER -2020**

17MA1201 - Laplace Transforms & Integral Calculus

188W1A0128 198W1A05H8 198W1A0212 178W1A1285 198W1A0369 198W1A03H9

17PH1202A - Engineering Physics

- NIL -

17PH1202C - Physics For Engineers

198W1A03E6

17CH1102A /1202A – Engineering Chemistry

198W1A0530 198W1A0548 198W1A0567 198W1A04F7 198W1A1067 198W1A1077
188W1A1294 198W1A03D8

17CS1203 – Programming in C

198W1A0107 198W1A0544 198W1A0550 198W1A04F7 198W1A04H8 198W1A04M1

17EC1204A – Basic Electronic Engineering

198W1A0530 198W1A05F8 198W1A05H8 178W1A1227 198W1A1231 198W1A1237

17EC1204B- Electronic Devices

- NIL -

17EE1204 – Network Analysis -I

- NIL -

17EI1204 - Electronics Devices & Circuits

- NIL -

17ME1204 – Engineering Mechanics -II

- NIL -

17ME1105/17ME1205 – Engineering Graphics

- NIL -

17HS1105/17HS1205 - Technical English & Communication Skills

198W1A03G2 198W1A03H4

17MA1101 - Matrices & Differential Calculus

- NIL -

17CS1103 – Problem Solving Method


- NIL -


17EE1104 - Basics of Electrical Engineering

188W1A04B7

17ME1104A – Engineering Mechanics –I

- NIL -


CONTROLLER OF EXAMINATIONS
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