

VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE (AUTONOMOUS)
VIJAYAWADA- 520 007
1/4 B.Tech 1 & 2 SEMESTER (SUPPLEMENTARY) PRACTICAL EXAMINATION TIME TABLE
APRIL - 2021

DATE	TIME	17CS1152/17CS1252 Computing and peripherals lab/ C Programming Lab	EXAMINATION HALL
10.04.2021	01.00 PM to 4.00 PM	SUPPLE	FMC-I

SUPPLE

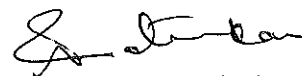
B.Tech ¼ Ist Sem Supple VR-17 Regulation:

198W1A01D4

B.Tech ¼ IInd Sem Supple VR-17 Regulation:

188W1A0104, 188W1A0127, 188W1A0374, 188W1A03A1, 188W1A03B3,
188W1A05F2, 198W1A0123, 198W1A0125, 198W1A0126, 198W1A0133,
198W1A0139, 198W1A0163, 198W1A0169, 198W1A01D4, 198W1A01D5,
198W1A01F0, 198W1A01F1, 198W1A01G9, 198W1A0347, 198W1A0409,
198W1A0485, 198W1A0488, 198W1A04O4, 198W1A05E9, 198W1A1020.

Yours sincerely,



(Dr.A.Ratnakar)
In-Charge, I/IV B.Tech

INCHARGE

First Year B.Tech

V.R. Siddhartha Engineering College
VIJAYAWADA - 520 007



PRINCIPAL

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